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(19) **United States**(12) **Patent Application Publication**  
**JEONG et al.**(10) **Pub. No.: US 2019/0198578 A1**(43) **Pub. Date: Jun. 27, 2019**(54) **ORGANIC LIGHT EMITTING DIODE  
DISPLAY INCLUDING CAPPING LAYER  
HAVING OPTICAL THICKNESS FOR  
IMPROVING OPTICS**(71) Applicant: **Samsung Display Co., Ltd.**, Yongin-si  
(KR)(72) Inventors: **Hee Seong JEONG**, Seoul (KR); **Won  
Ju KWON**, Cheonan-si (KR); **Dal Ho  
KIM**, Hwaseong-si (KR); **Gi-Na YOO**,  
Yongin-si (KR); **Dong Jin LEE**, Seoul  
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(2013.01); **H01L 51/5016** (2013.01); **H01L**  
**51/5056** (2013.01)(57) **ABSTRACT**

An OLED display including first, second, and third color pixels each including a first electrode, an organic emission layer, a second electrode, and a capping layer disposed on the disposed on the substrate, in which the first color pixel is configured to emit green light, and the second and third color pixels are each configured to emit a color of light other than green, the organic emission layer of the first color pixel includes a first emission layer and a second emission layer each being configured to emit light, the organic emission layer of the second color pixel or in the third color pixel includes a third emission layer configured to emit light, the second emission layer and the third emission layer include both a host and a dopant, and the first emission layer includes the host, and does not include any dopants therein.

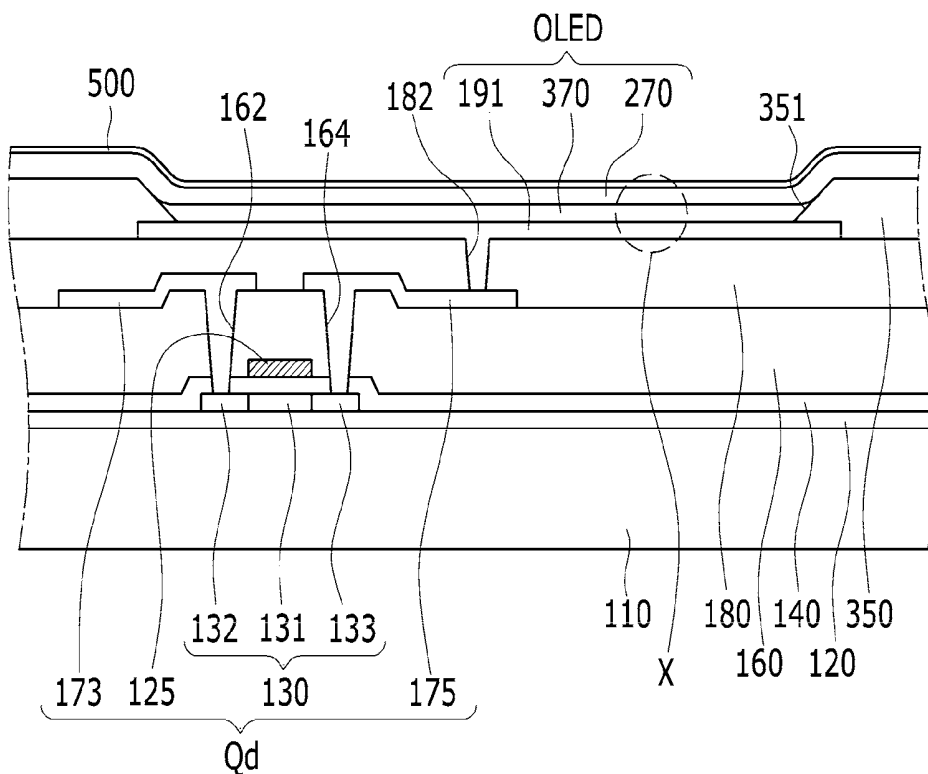


FIG. 1

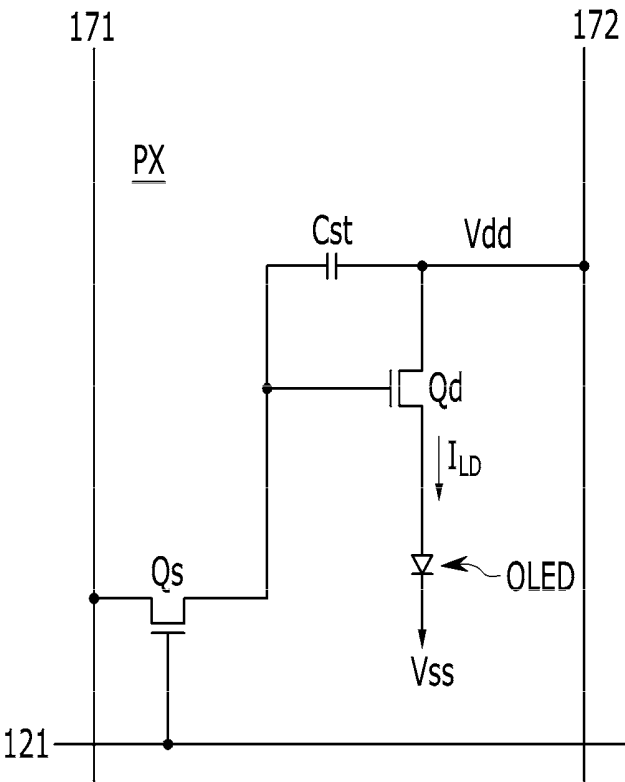


FIG. 2

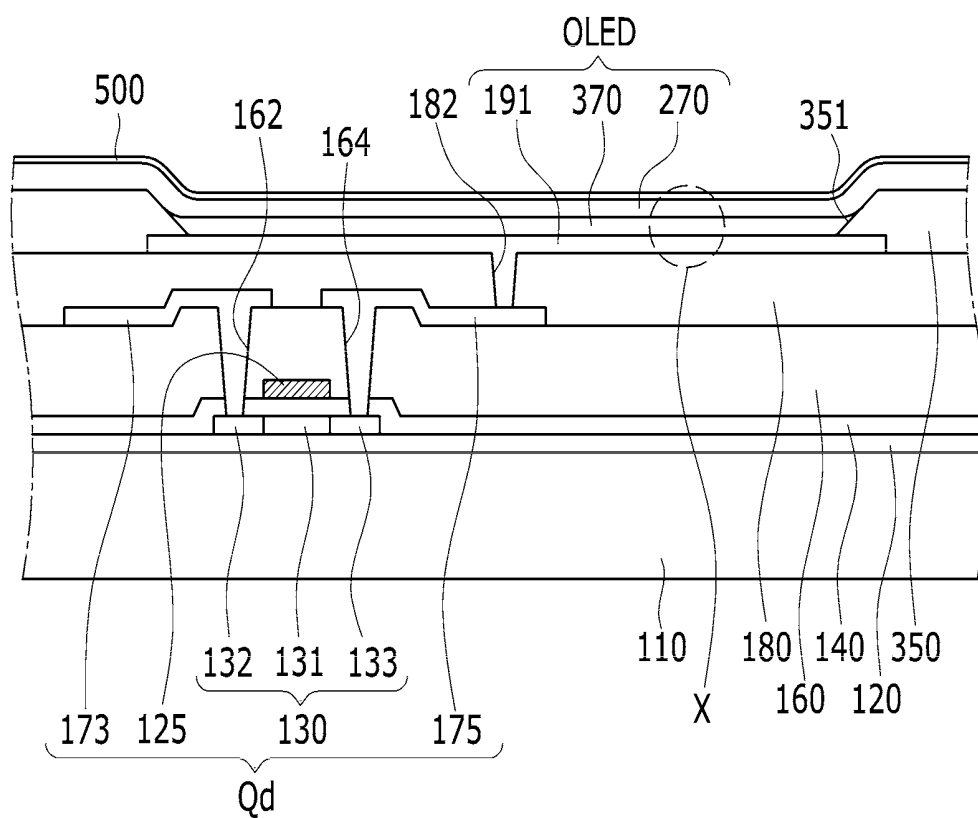


FIG. 3

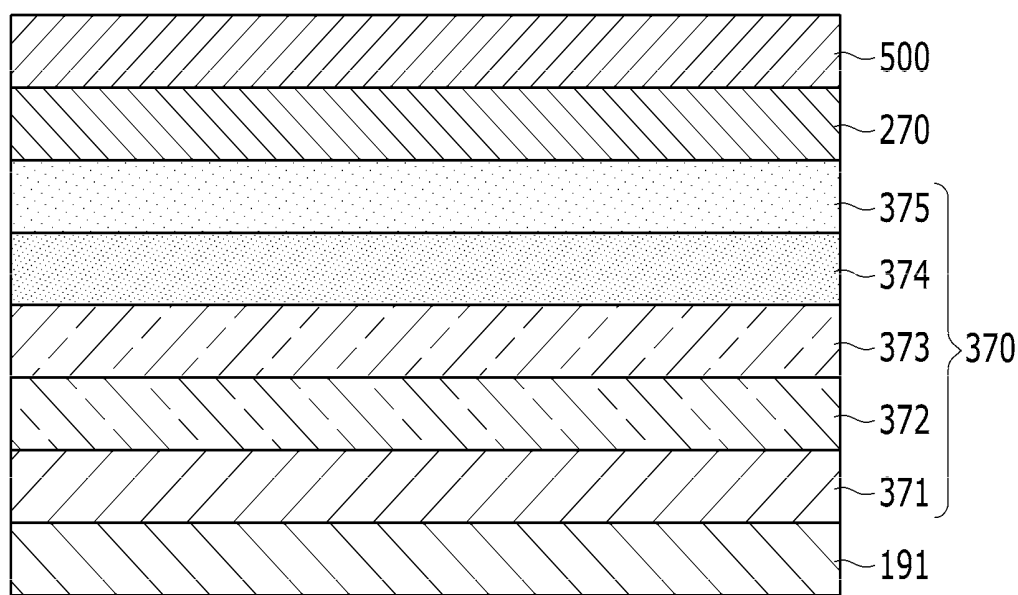


FIG. 4

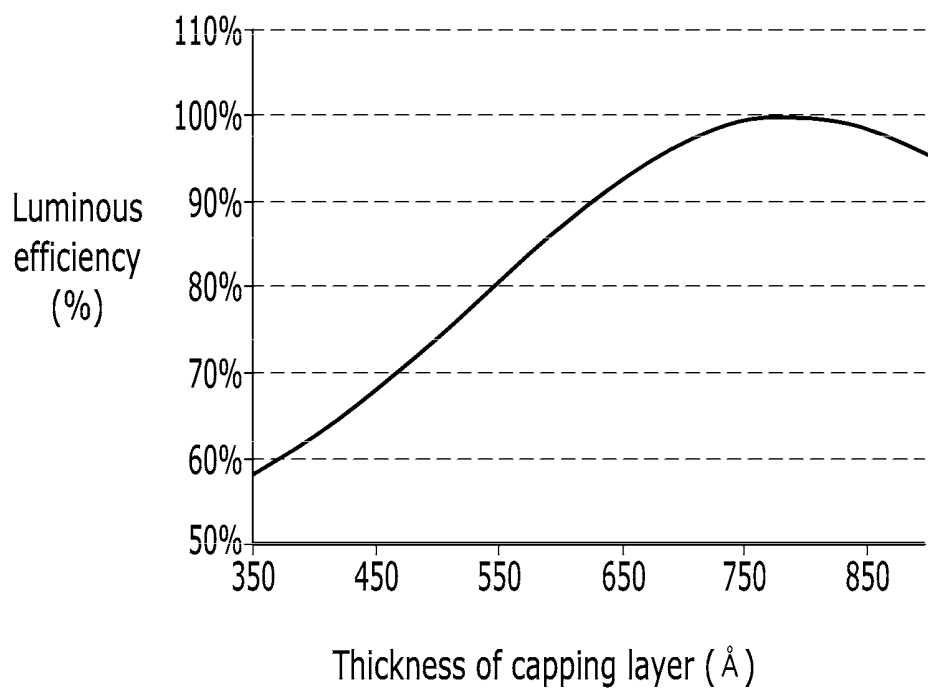


FIG. 5

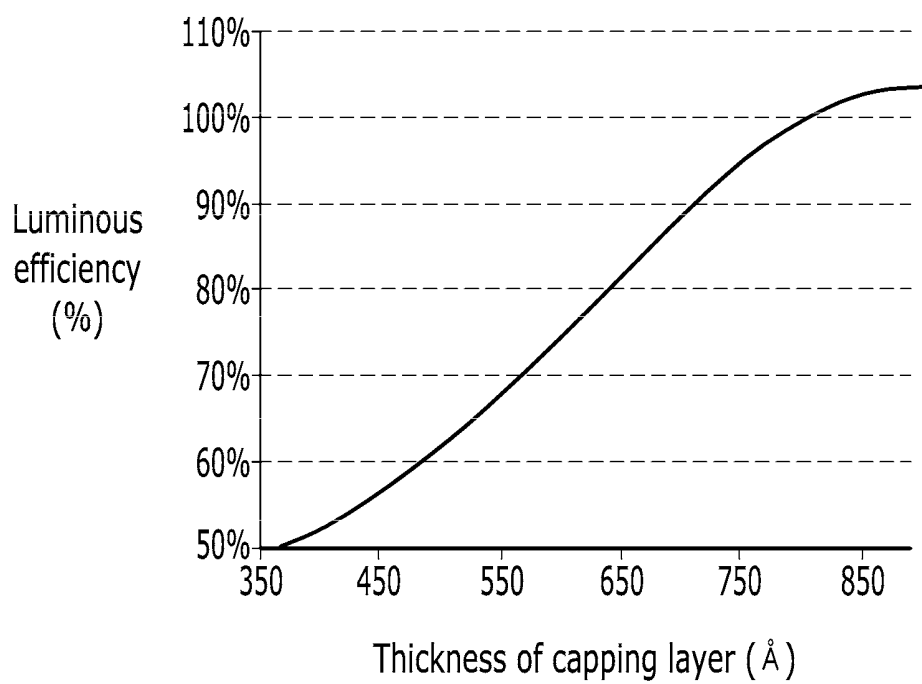


FIG. 6

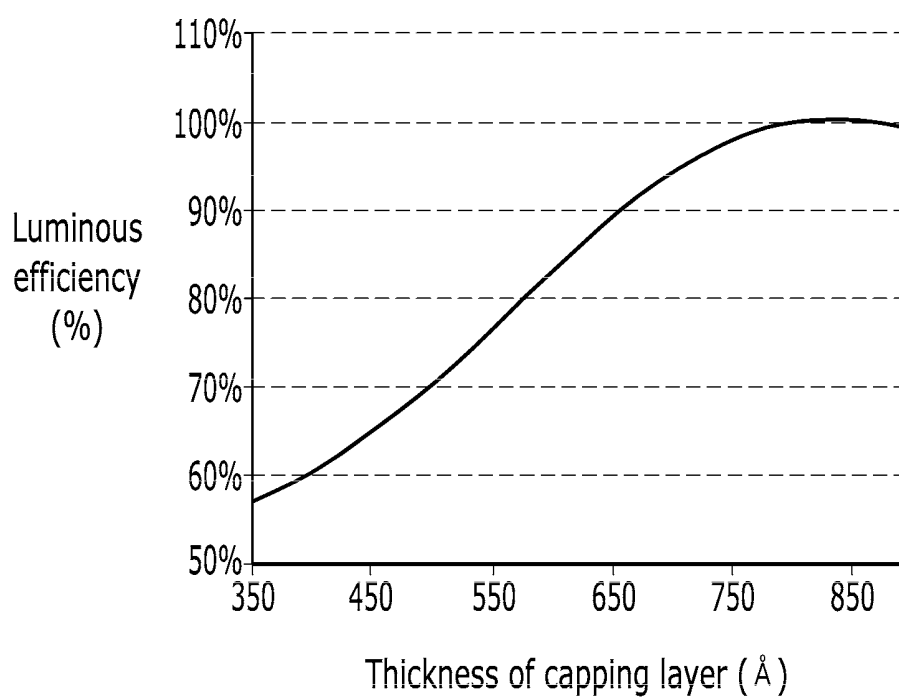


FIG. 7

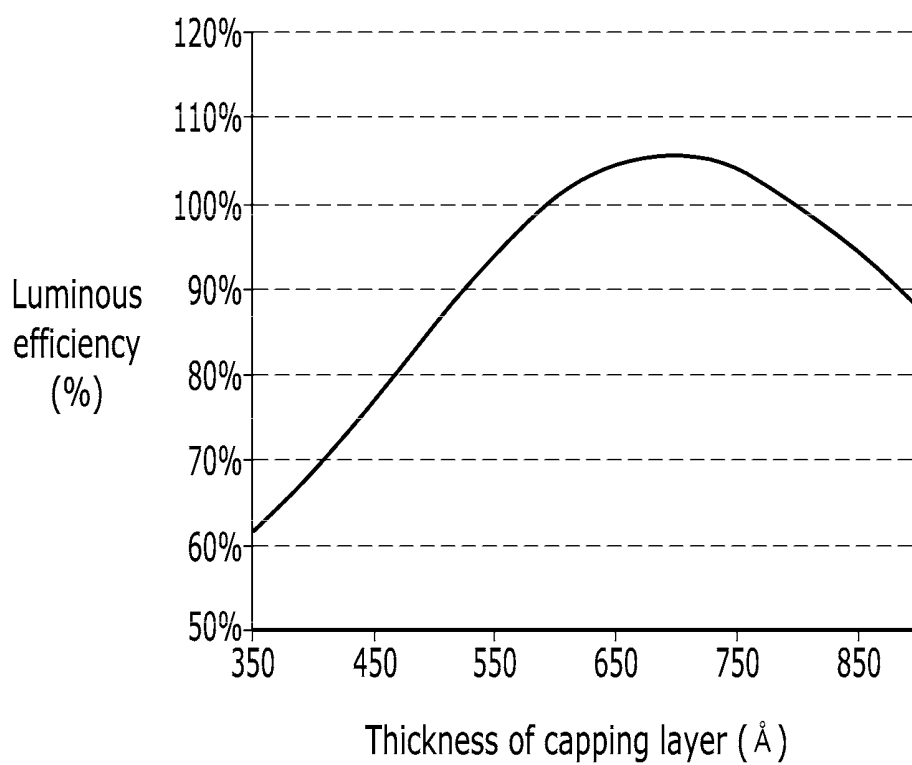


FIG. 8

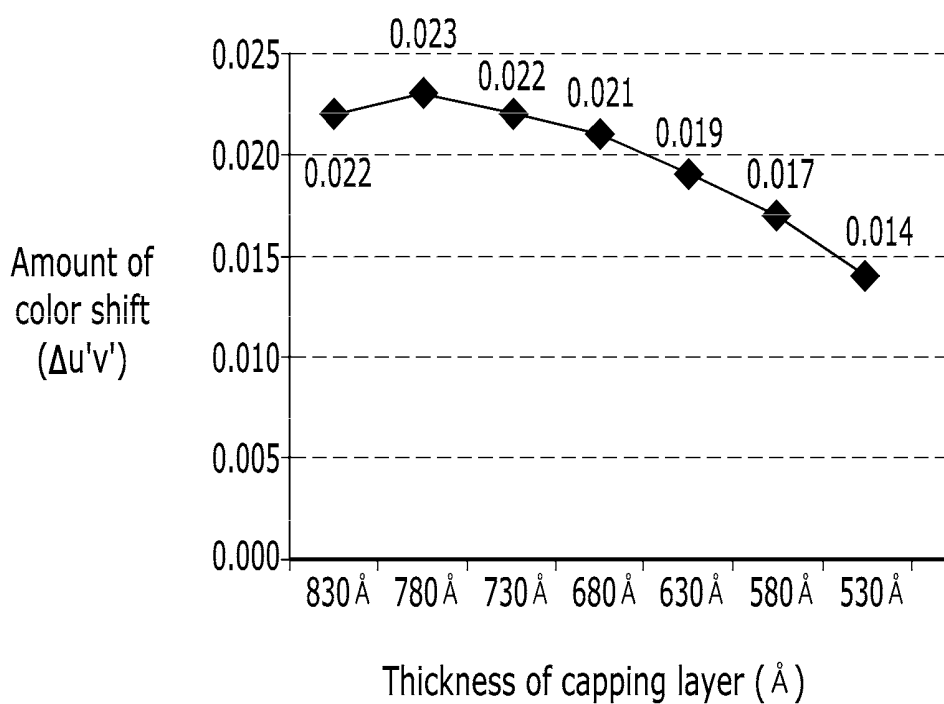


FIG. 9

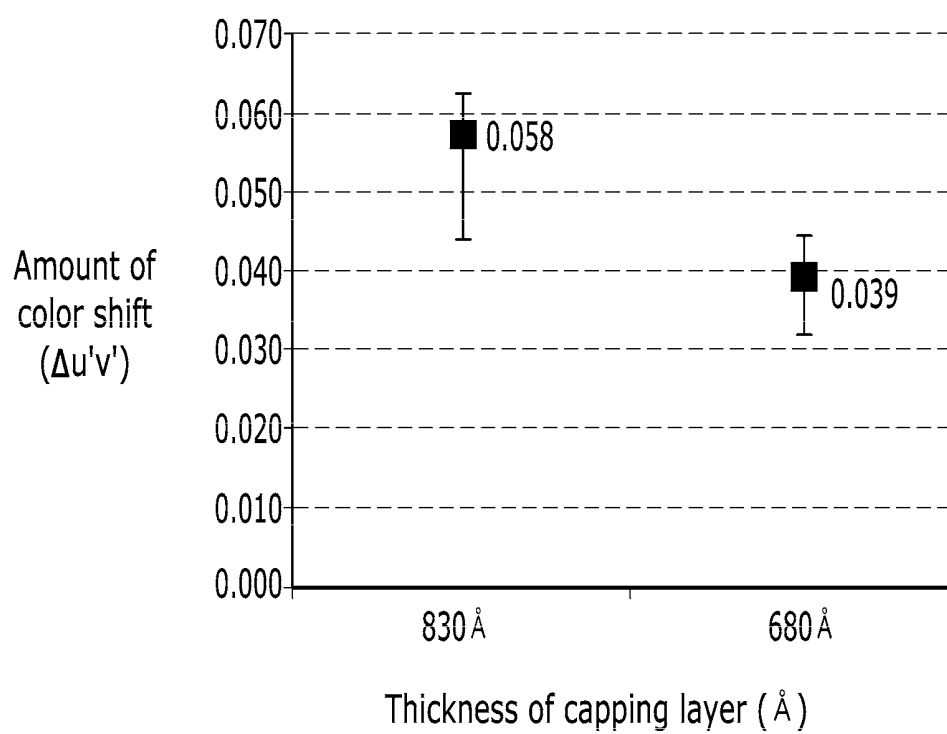


FIG. 10

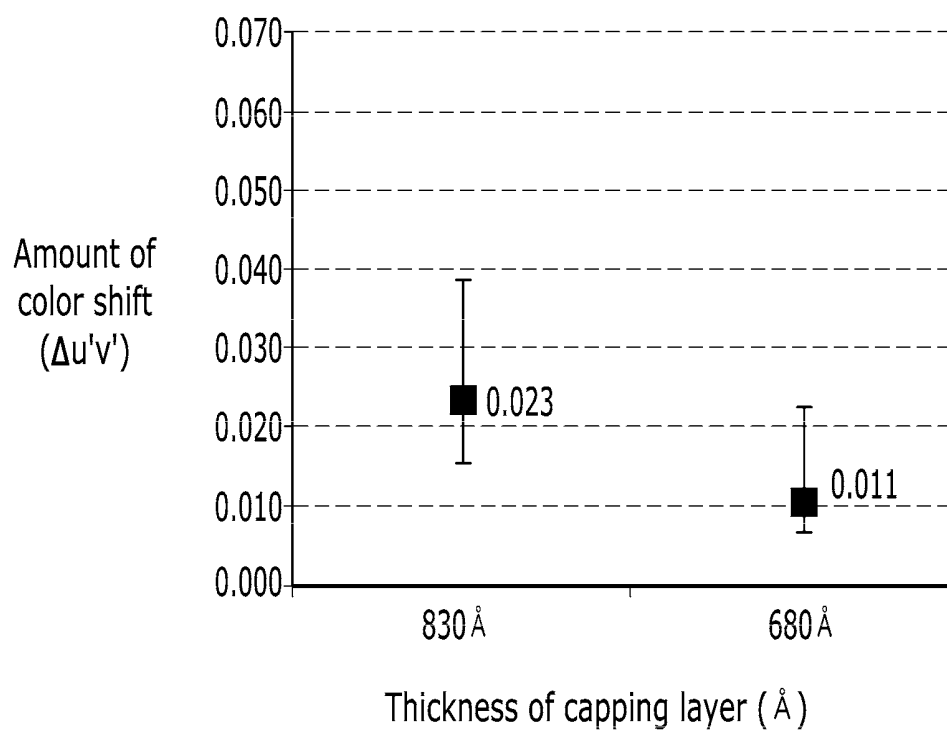


FIG. 11

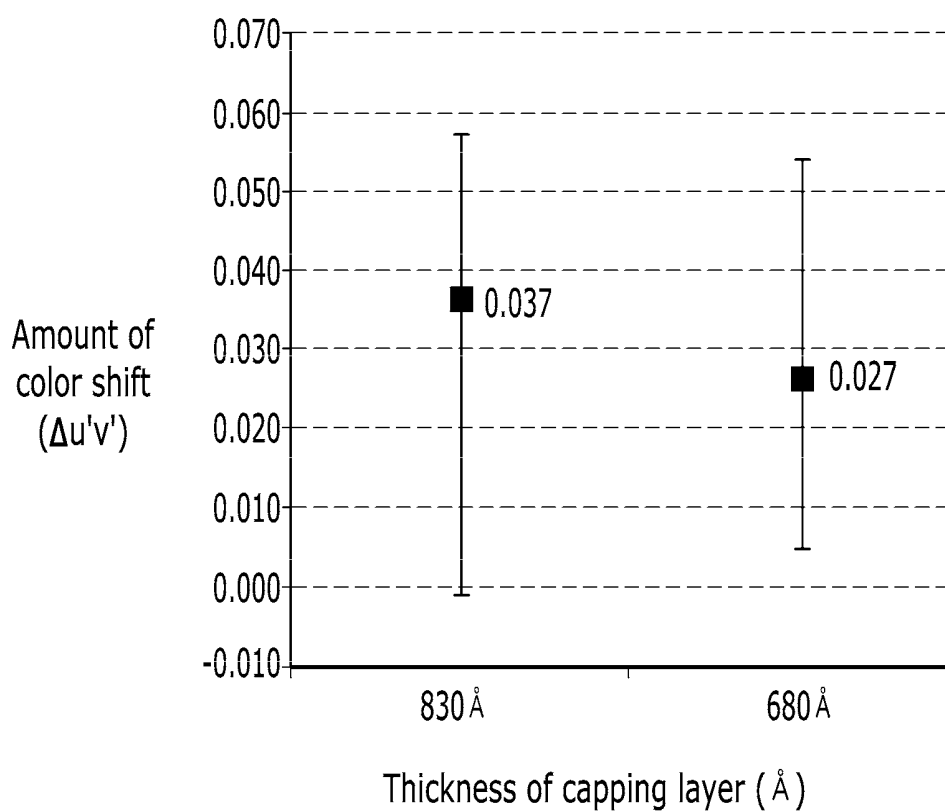


FIG. 12

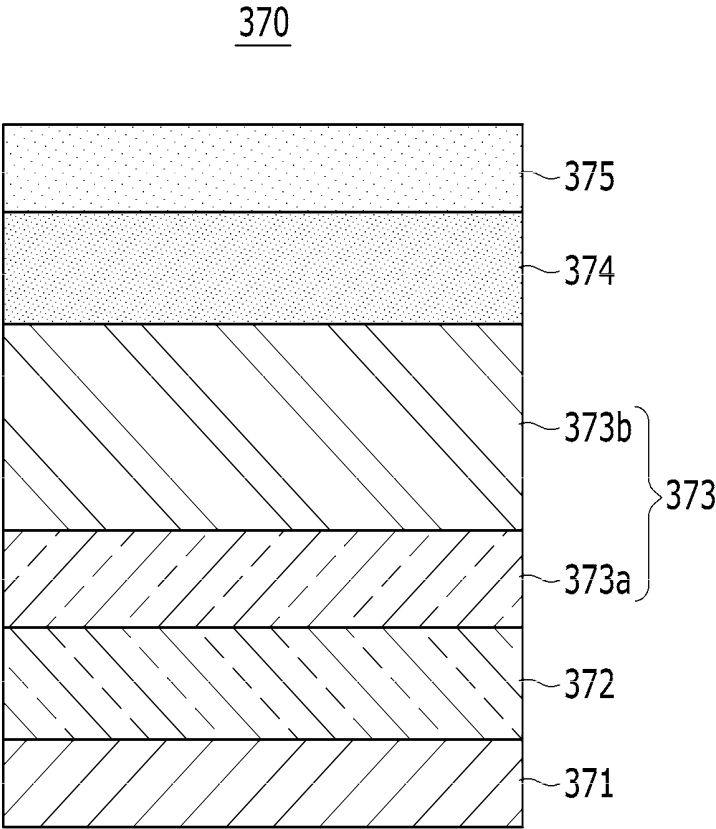


FIG. 13

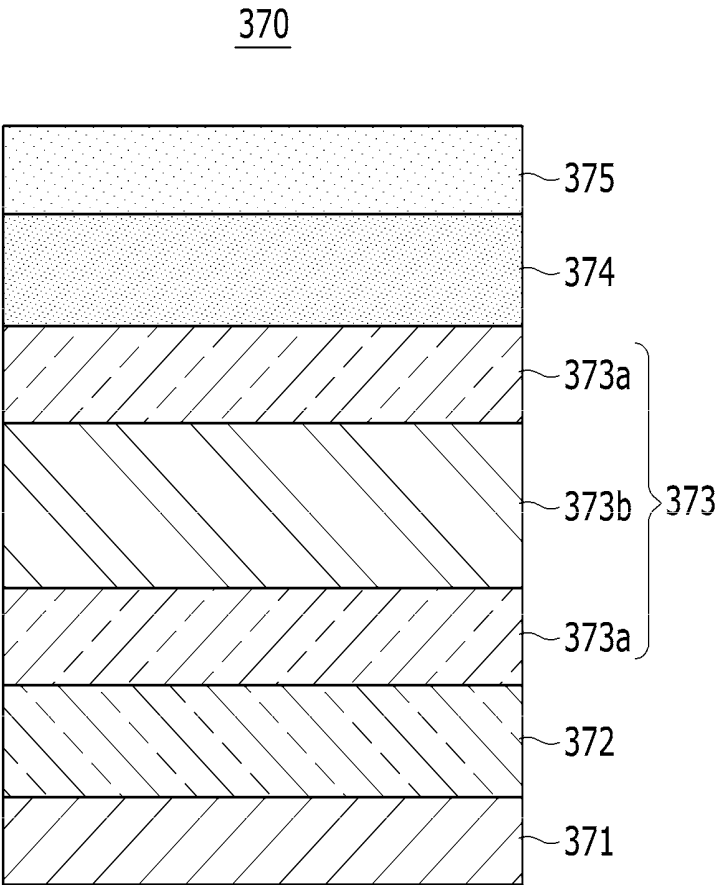


FIG. 14

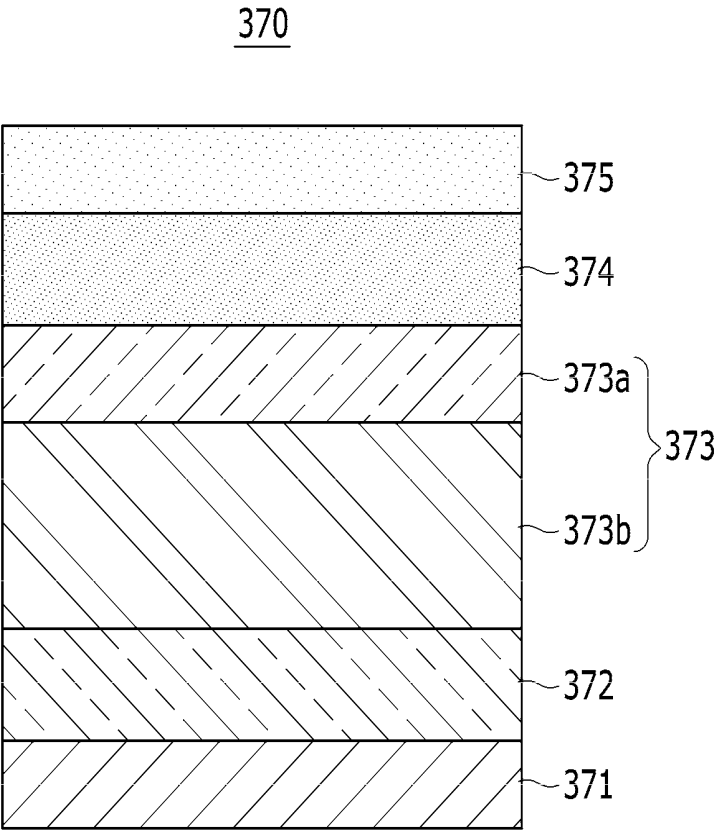


FIG. 15

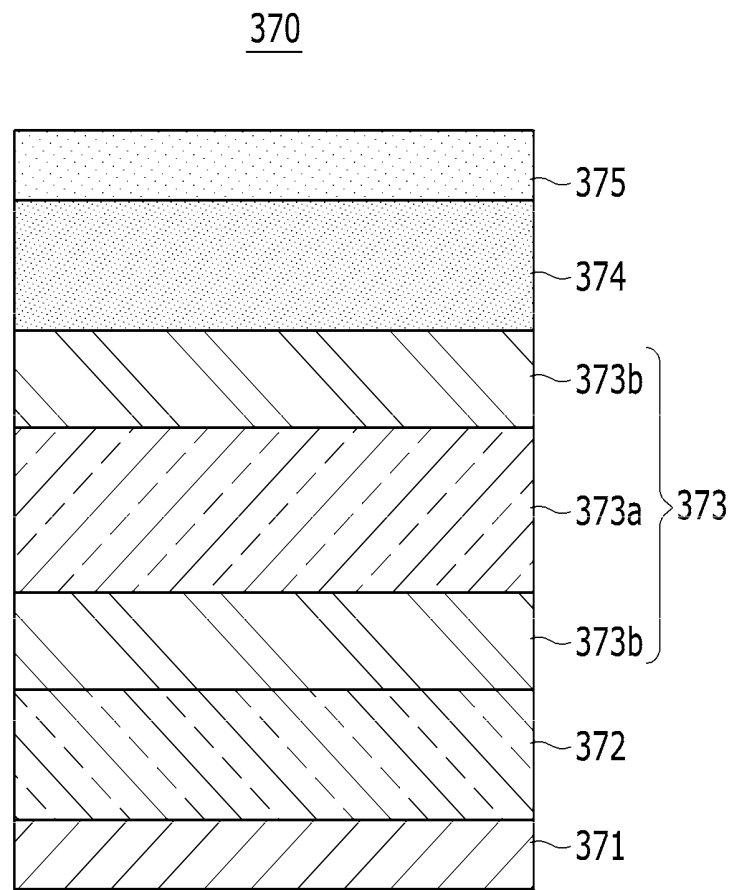


FIG. 16

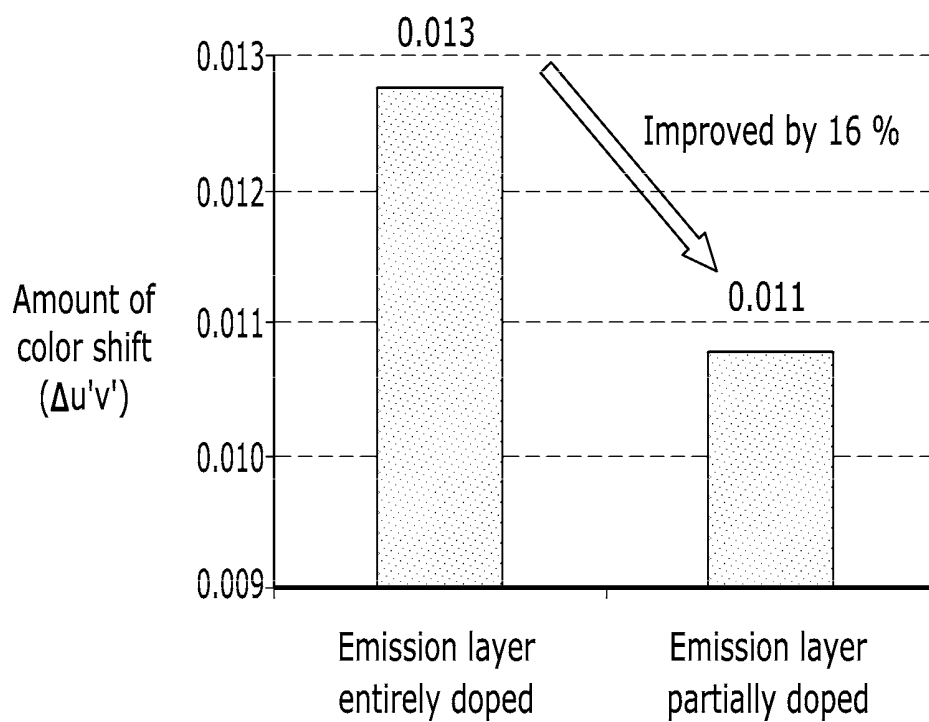


FIG. 17

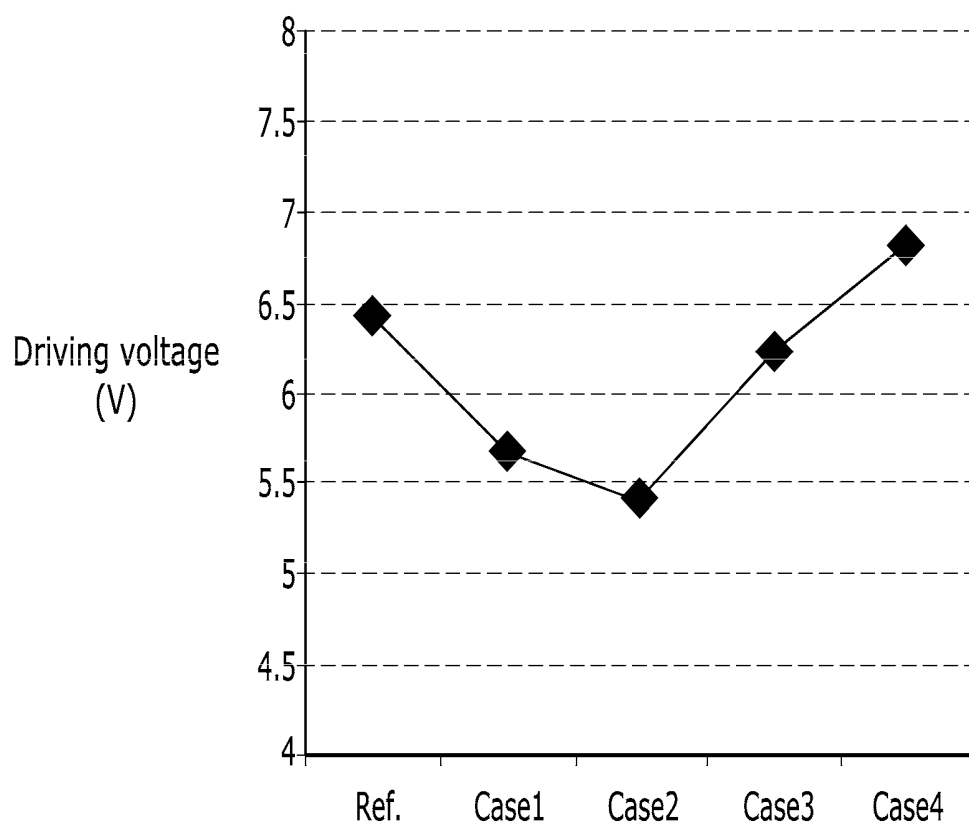


FIG. 18

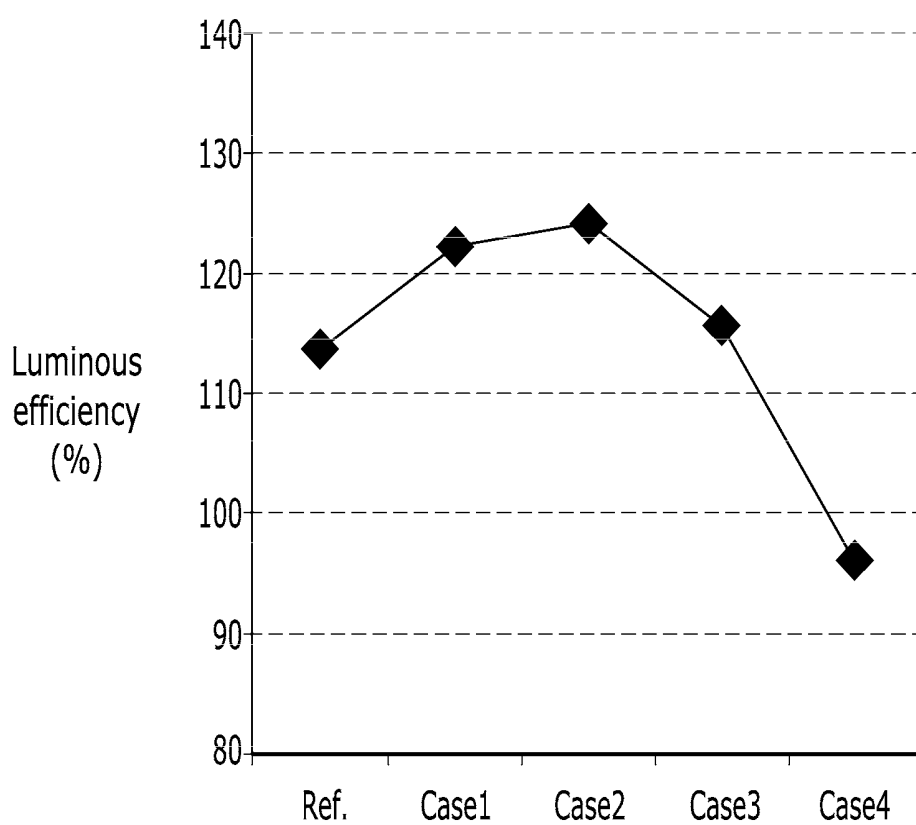


FIG. 19

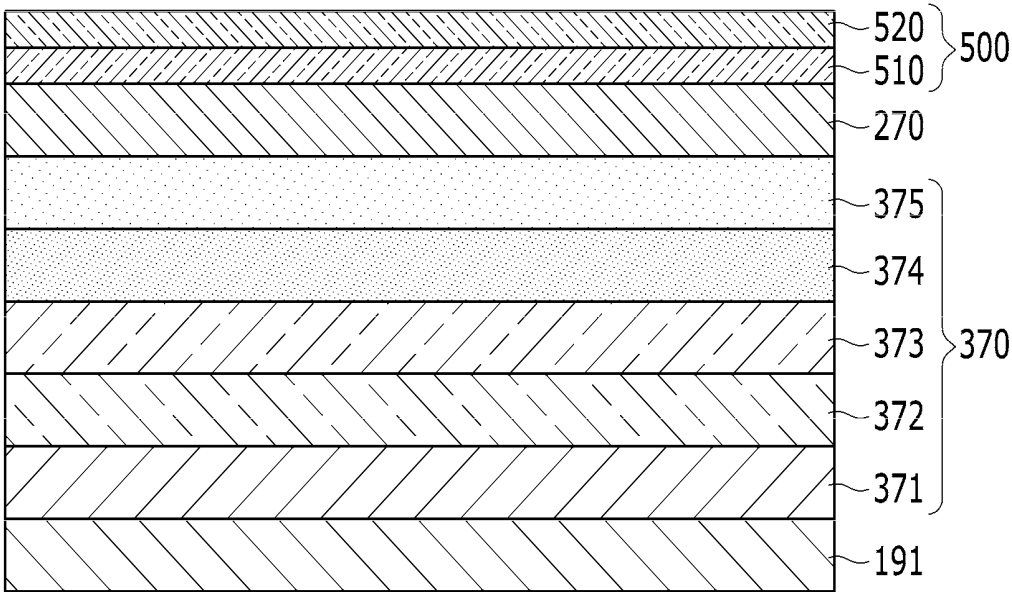


FIG. 20

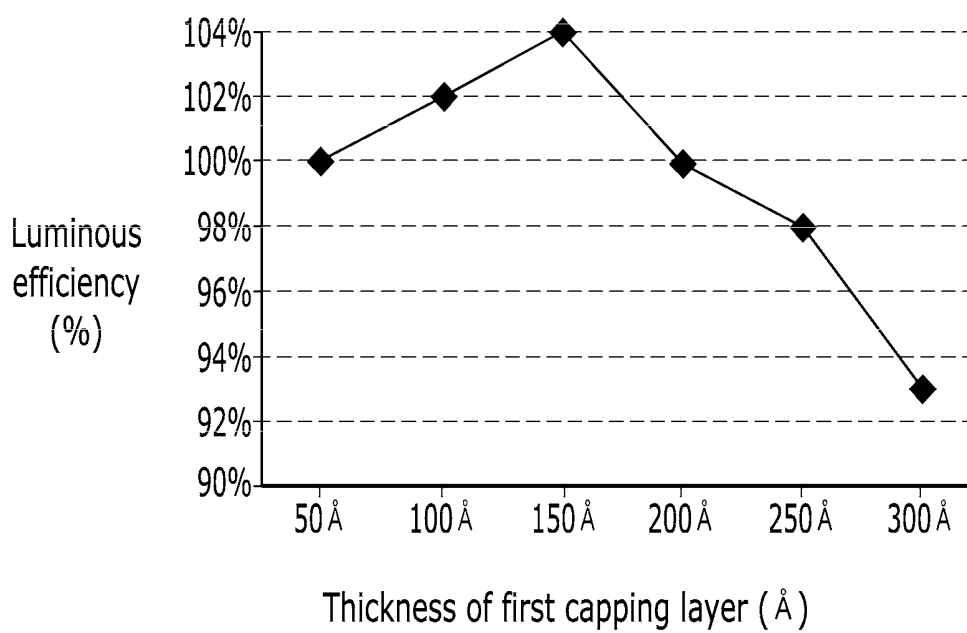


FIG. 21

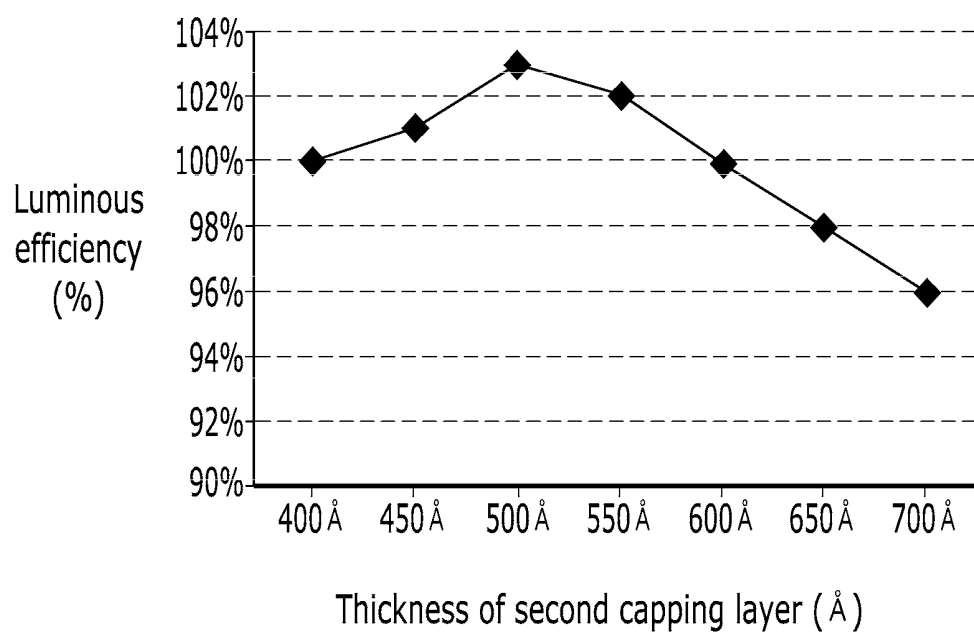


FIG. 22

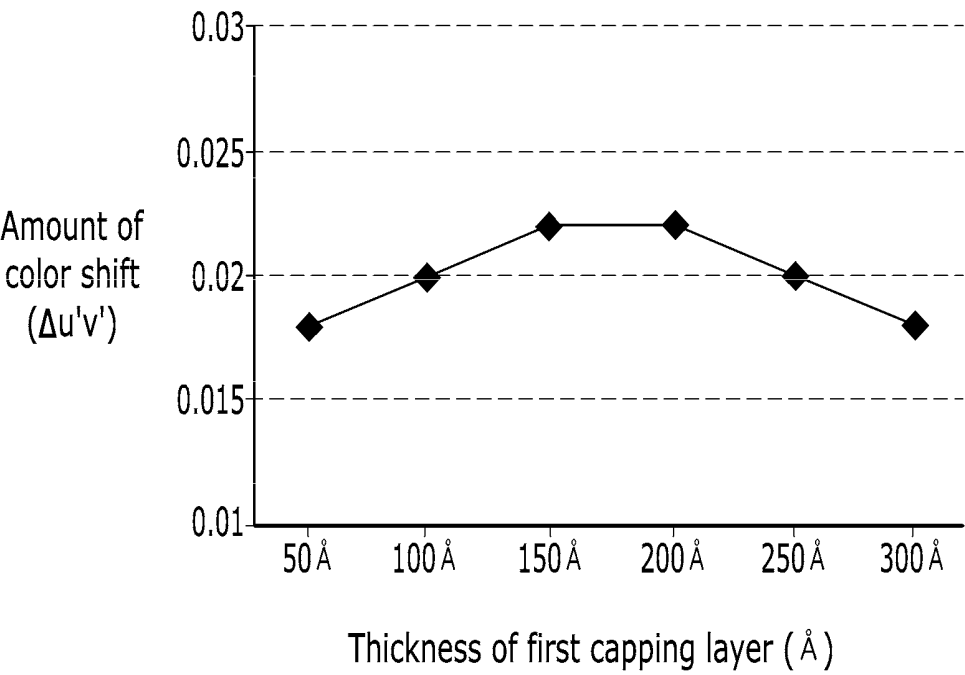


FIG. 23

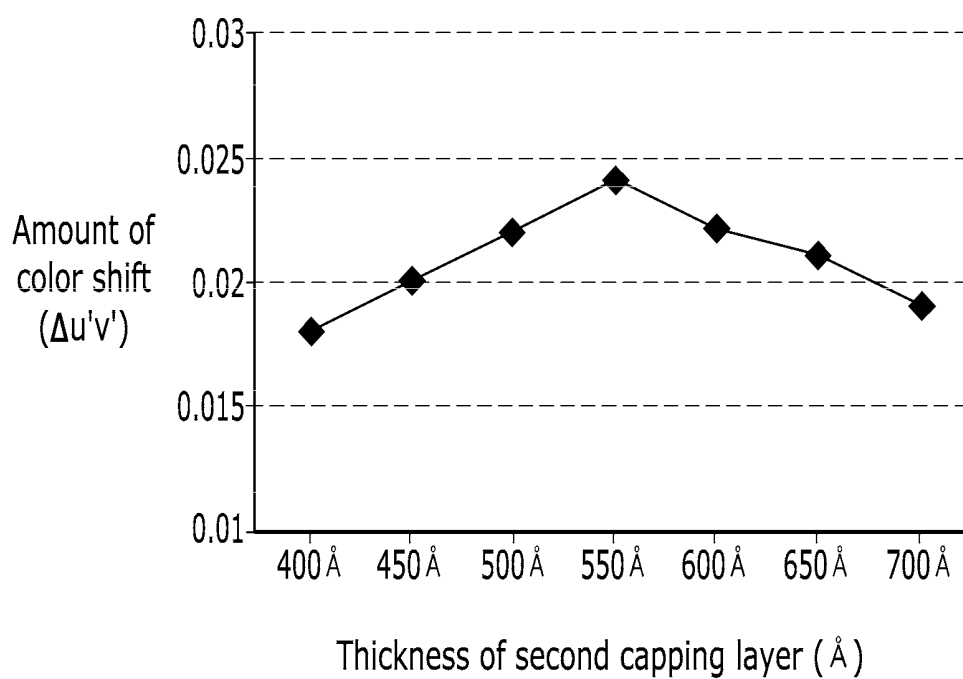
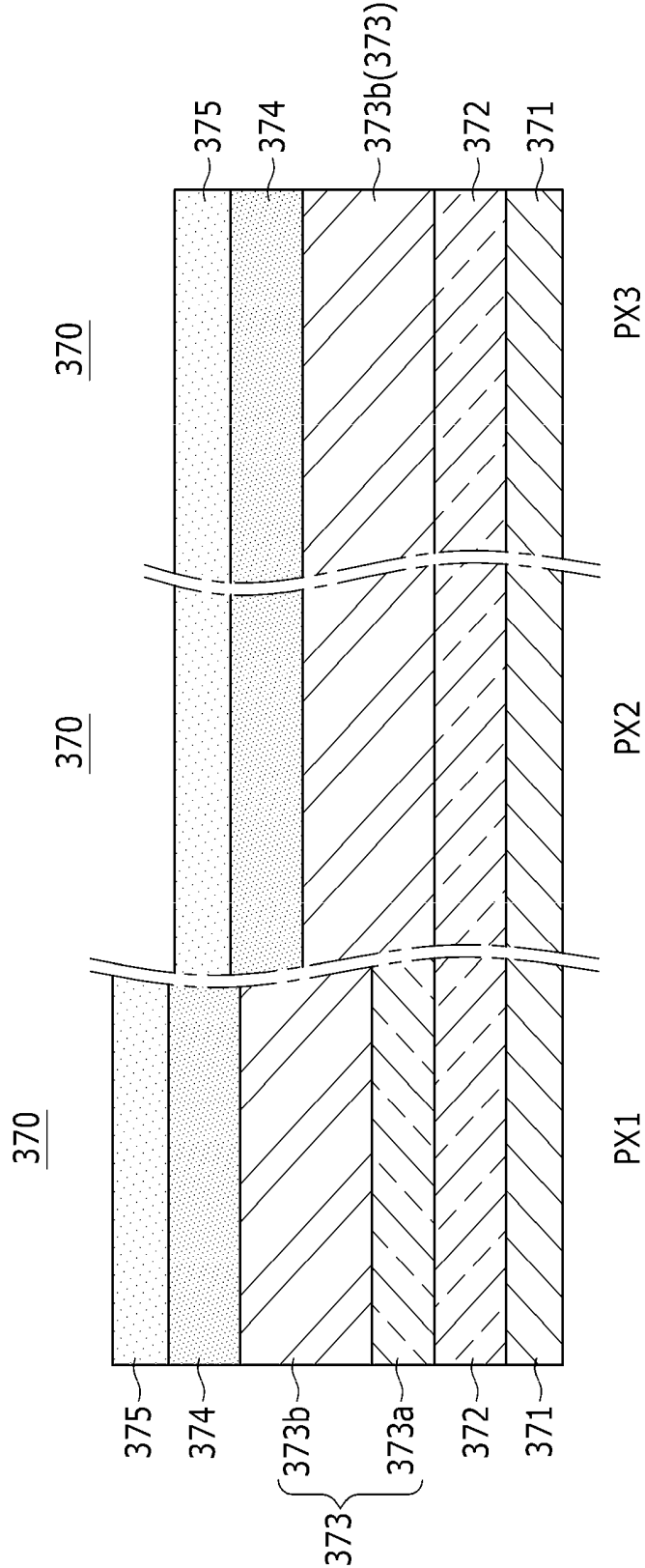


FIG. 24



**ORGANIC LIGHT EMITTING DIODE  
DISPLAY INCLUDING CAPPING LAYER  
HAVING OPTICAL THICKNESS FOR  
IMPROVING OPTICS**

**CROSS-REFERENCE TO RELATED  
APPLICATION**

[0001] This application is a continuation of U.S. application Ser. No. 15/097,871, filed on Apr. 13, 2016, and claims priority from and the benefit of Korean Patent Application No. 10-2015-0138975, filed in on Oct. 2, 2015, which are hereby incorporated by reference for all purposes as if fully set forth herein.

**BACKGROUND**

**Field**

[0002] Exemplary embodiments relate to an organic light emitting diode (OLED) display, and, more particularly, to an OLED display with increased luminous efficiency and improved viewing angle.

**Discussion of the Background**

[0003] An organic light emitting diode (OLED) display may include two electrodes and an organic emission layer interposed therebetween. The electrons injected from one electrode and holes injected from the other electrode may be combined in the organic emission layer to generate excitons. Light may be emitted as the excitons release energy, when the excitons change from an excited state to a ground state.

[0004] An OLED display includes pixels including an OLED, as a self-emissive element, transistors for driving the OLED, and at least one capacitor formed in each pixel. The transistors may generally include a switching transistor and a driving transistor.

[0005] A capping layer may be formed on the electrodes to protect the electrodes of the OLED display. Accordingly, luminous efficiency of light emitted from the organic emission layer may be affected by the capping layer. However, as luminous efficiency increases, strong resonance may occur, which may generate color shift associated with a viewing angle. That is, a difference in color may appear between front and side views of a screen.

[0006] The above information disclosed in this Background section is only for enhancement of understanding of the background of the inventive concept, and, therefore, it may contain information that does not form the prior art that is already known in this country to a person of ordinary skill in the art.

**SUMMARY**

[0007] Exemplary embodiments provide an organic light emitting diode (OLED) display that may improve luminous efficiency thereof.

[0008] Exemplary embodiments also provide an OLED display that may improve a viewing angle by reducing color shift associated with a viewing angle.

[0009] Additional aspects will be set forth in the detailed description which follows, and, in part, will be apparent from the disclosure, or may be learned by practice of the inventive concept.

[0010] According to an exemplary embodiment of the present invention, an organic light emitting diode (OLED)

display includes a substrate, a first electrode disposed on the substrate, an organic emission layer disposed on the first electrode, a second electrode disposed on the organic emission layer, and a capping layer disposed on the second electrode, in which an optical thickness of the capping layer is in a range of about 1100 Å to about 1400 Å.

[0011] According to an exemplary embodiment of the present invention, an organic light emitting diode (OLED) display includes a substrate, a first electrode disposed on the substrate, an organic emission layer disposed on the first electrode, a second electrode disposed on the organic emission layer, and a capping layer disposed on the second electrode, the capping layer including a first capping layer and a second capping layer having different refractive indices from each other, in which an optical thickness of the first capping layer is in a range of about 60 Å to 210 Å, and an optical thickness of the second capping layer is in a range of about 820 Å to about 1030 Å.

[0012] According to an exemplary embodiment of the present invention, an organic light emitting diode (OLED) display includes a first color pixel, a second color pixel, and a third color pixel on a substrate, each of the first, second, and third color pixels including a first electrode disposed on the substrate, an organic emission layer disposed on the first electrode, a second electrode disposed on the organic emission layer, and a capping layer disposed on the second electrode, in which the first color pixel is configured to emit green light, and the second and third color pixels are each configured to emit a color of light other than green, the organic emission layer of the first color pixel includes a first emission layer and a second emission layer each being configured to emit light, the organic emission layer of the second color pixel or the third color pixel includes a third emission layer configured to emit light, the second emission layer and the third emission layer include both a host and a dopant, and the first emission layer includes the host, and does not include any dopants therein.

[0013] According to exemplary embodiments of the present invention, an optical thickness of the capping layer may be controlled to improve the luminous efficiency and the viewing angle. In addition, the luminous efficiency and the viewing angle may be improved by including the host and the dopant in a first portion of regions of the emission layer and only the host in a second portion of regions thereof.

[0014] The foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the claimed subject matter.

**BRIEF DESCRIPTION OF THE DRAWINGS**

[0015] The accompanying drawings, which are included to provide a further understanding of the inventive concept, and are incorporated in and constitute a part of this specification, illustrate exemplary embodiments of the inventive concept, and, together with the description, serve to explain principles of the inventive concept.

[0016] FIG. 1 is an equivalent circuit diagram of one pixel of an organic light emitting diode (OLED) display according to an exemplary embodiment of the present invention.

[0017] FIG. 2 is a cross-sectional view of an OLED display according to an exemplary embodiment of the present invention.

[0018] FIG. 3 is an enlarged cross-sectional view of an OLED of an OLED display according to an exemplary embodiment of the present invention.

[0019] FIG. 4 is a graph showing luminous efficiency of white light associated with a thickness of a capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0020] FIG. 5 is a graph showing luminous efficiency of red light associated with a thickness of a capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0021] FIG. 6 is a graph showing luminous efficiency of green light associated with a thickness of a capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0022] FIG. 7 is a graph showing luminous efficiency of blue light associated with a thickness of a capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0023] FIG. 8 is a graph showing an amount of color shift for white associated with a thickness of a capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0024] FIG. 9 is a graph showing an amount of color shift for red associated with a thickness of a capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0025] FIG. 10 is a graph showing an amount of color shift for green associated with a thickness of a capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0026] FIG. 11 is a graph showing an amount of color shift for blue associated with a thickness of a capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0027] FIG. 12 is a cross-sectional view of an organic emission layer of an OLED display according to an exemplary embodiment of the present invention.

[0028] FIG. 13 is a cross-sectional view of an organic emission layer of an OLED display according to an exemplary embodiment of the present invention.

[0029] FIGS. 14 and 15 are cross-sectional views of an organic emission layer of an OLED display according to exemplary embodiments of the present invention.

[0030] FIG. 16 is a graph showing an amount of color shift associated with a structure of an emission layer of an OLED display according to an exemplary embodiment of the present invention.

[0031] FIG. 17 is a graph showing a driving voltage associated with the structure of an emission layer of an OLED display according to an exemplary embodiment of the present invention.

[0032] FIG. 18 is a graph showing luminous efficiency associated with the structure of an emission layer of an OLED display according to an exemplary embodiment of the present invention.

[0033] FIG. 19 is a cross-sectional view of an OLED of an OLED display according to an exemplary embodiment of the present invention.

[0034] FIG. 20 is a graph showing luminous efficiency associated with a thickness of a first capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0035] FIG. 21 is a graph showing luminous efficiency associated with a thickness of a second capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0036] FIG. 22 is a graph showing an amount of color shift associated with the thickness of a first capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0037] FIG. 23 is a graph showing an amount of color shift associated with the thickness of a second capping layer of an OLED display according to an exemplary embodiment of the present invention.

[0038] FIG. 24 is a cross-sectional view of an organic emission layer of an OLED display according to an exemplary embodiment of the present invention.

#### DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0039] In the following description, for the purposes of explanation, numerous specific details are set forth in order to provide a thorough understanding of various exemplary embodiments. It is apparent, however, that various exemplary embodiments may be practiced without these specific details or with one or more equivalent arrangements. In other instances, well-known structures and devices are shown in block diagram form in order to avoid unnecessarily obscuring various exemplary embodiments.

[0040] In the accompanying figures, the size and relative sizes of layers, films, panels, regions, etc., may be exaggerated for clarity and descriptive purposes. Also, like reference numerals denote like elements.

[0041] When an element or layer is referred to as being “on,” “connected to,” or “coupled to” another element or layer, it may be directly on, connected to, or coupled to the other element or layer or intervening elements or layers may be present. When, however, an element or layer is referred to as being “directly on,” “directly connected to,” or “directly coupled to” another element or layer, there are no intervening elements or layers present. For the purposes of this disclosure, “at least one of X, Y, and Z” and “at least one selected from the group consisting of X, Y, and Z” may be construed as X only, Y only, Z only, or any combination of two or more of X, Y, and Z, such as, for instance, XYZ, XYY, YZ, and ZZ. Like numbers refer to like elements throughout. As used herein, the term “and/or” includes any and all combinations of one or more of the associated listed items.

[0042] Although the terms first, second, etc. may be used herein to describe various elements, components, regions, layers, and/or sections, these elements, components, regions, layers, and/or sections should not be limited by these terms. These terms are used to distinguish one element, component, region, layer, and/or section from another element, component, region, layer, and/or section. Thus, a first element, component, region, layer, and/or section discussed below could be termed a second element, component, region, layer, and/or section without departing from the teachings of the present disclosure.

[0043] Spatially relative terms, such as “beneath,” “below,” “lower,” “above,” “upper,” and the like, may be used herein for descriptive purposes, and, thereby, to describe one element or feature’s relationship to another element(s) or feature(s) as illustrated in the drawings. Spatially relative terms are intended to encompass different

orientations of an apparatus in use, operation, and/or manufacture in addition to the orientation depicted in the drawings. For example, if the apparatus in the drawings is turned over, elements described as “below” or “beneath” other elements or features would then be oriented “above” the other elements or features. Thus, the exemplary term “below” can encompass both an orientation of above and below. Furthermore, the apparatus may be otherwise oriented (e.g., rotated 90 degrees or at other orientations), and, as such, the spatially relative descriptors used herein interpreted accordingly.

[0044] The terminology used herein is for the purpose of describing particular embodiments and is not intended to be limiting. As used herein, the singular forms, “a,” “an,” and “the” are intended to include the plural forms as well, unless the context clearly indicates otherwise. Moreover, the terms “comprises,” “comprising,” “includes,” and/or “including,” when used in this specification, specify the presence of stated features, integers, steps, operations, elements, components, and/or groups thereof, but do not preclude the presence or addition of one or more other features, integers, steps, operations, elements, components, and/or groups thereof.

[0045] Various exemplary embodiments are described herein with reference to sectional illustrations that are schematic illustrations of idealized exemplary embodiments and/or intermediate structures. As such, variations from the shapes of the illustrations as a result, for example, of manufacturing techniques and/or tolerances, are to be expected. Thus, exemplary embodiments disclosed herein should not be construed as limited to the particular illustrated shapes of regions, but are to include deviations in shapes that result from, for instance, manufacturing. For example, an implanted region illustrated as a rectangle will, typically, have rounded or curved features and/or a gradient of implant concentration at its edges rather than a binary change from implanted to non-implanted region. Likewise, a buried region formed by implantation may result in some implantation in the region between the buried region and the surface through which the implantation takes place. Thus, the regions illustrated in the drawings are schematic in nature and their shapes are not intended to illustrate the actual shape of a region of a device and are not intended to be limiting.

[0046] Unless otherwise defined, all terms (including technical and scientific terms) used herein have the same meaning as commonly understood by one of ordinary skill in the art to which this disclosure is a part. Terms, such as those defined in commonly used dictionaries, should be interpreted as having a meaning that is consistent with their meaning in the context of the relevant art and will not be interpreted in an idealized or overly formal sense, unless expressly so defined herein.

[0047] Hereinafter, one pixel of an organic light emitting diode (OLED) display according to an exemplary embodiment of the present invention will be described.

[0048] FIG. 1 is an equivalent circuit diagram of one pixel of an OLED display according to an exemplary embodiment of the present invention.

[0049] Referring to FIG. 1, the OLED display according to the present exemplary embodiment includes signal lines 121, 171, and 172, and a pixel PX connected thereto. Although not illustrated, pixels PX may be arranged in a matrix form that includes pixel rows and pixel columns. In

this case, the pixels PX are respectively connected to the signal lines 121, 171, and 172.

[0050] The signal lines 121, 171, and 172 include gate lines 121 for transmitting a gate signal (also referred to as a “scan signal”), data lines 171 for transmitting a data signal, and driving voltage lines 172 for transmitting a driving voltage V<sub>dd</sub>. Only one of the gate lines 121, one of the data lines 171, and one of the driving voltage lines 172 are illustrated in FIG. 1, but the gate line 121, data line 171, and driving voltage line 172 may be formed in plural. The gate lines 121 substantially extend in a row direction and are substantially parallel to each other. The data lines 171 and the driving voltage lines 172 extend in a column direction to cross the gate lines 121 and are substantially parallel to each other.

[0051] The OLED display according to the present exemplary embodiment includes a switching thin-film transistor Q<sub>s</sub>, a driving thin-film transistor Q<sub>d</sub>, a storage capacitor C<sub>st</sub>, and an organic light emitting diode (OLED).

[0052] The switching thin-film transistor Q<sub>s</sub> includes a control terminal, an input terminal, and an output terminal. The control terminal of the switching thin-film transistor Q<sub>s</sub> is connected to the gate line 121, the input terminal thereof is connected to the data line 171, and the output terminal thereof is connected to the driving thin-film transistor Q<sub>d</sub>. The switching thin-film transistor Q<sub>s</sub> transmits the data signal applied to the data line 171 to the driving thin-film transistor Q<sub>d</sub>, in response to the gate signal applied to the gate line 121.

[0053] The driving thin-film transistor Q<sub>d</sub> has a control terminal, an input terminal, and an output terminal. The control terminal of the driving thin-film transistor Q<sub>d</sub> is connected to the switching thin-film transistor Q<sub>s</sub>, the input terminal thereof is connected to the driving voltage line 172, and the output terminal thereof is connected to the OLED. The driving thin-film transistor Q<sub>d</sub> outputs an output current I<sub>LD</sub>, an amount of which varies depending on a voltage applied between the control terminal and the output terminal of the driving thin-film transistor Q<sub>d</sub>.

[0054] The storage capacitor C<sub>st</sub> is connected between the control terminal and input terminal of the driving thin-film transistor Q<sub>d</sub>. The storage capacitor C<sub>st</sub> is charged by the data signal applied to the control terminal of the driving thin-film transistor Q<sub>d</sub>, and maintains the data signal even after the switching thin-film transistor Q<sub>s</sub> is turned off.

[0055] The OLED has an anode connected to the output terminal of the driving thin-film transistor Q<sub>d</sub>, and a cathode connected to a common voltage V<sub>ss</sub>. The OLED displays an image by emitting light, the intensity of which varies depending on the output current I<sub>LD</sub> of the driving thin-film transistor Q<sub>d</sub>. The connection relationship between the switching thin-film transistor Q<sub>s</sub>, the driving thin-film transistor Q<sub>d</sub>, the storage capacitor C<sub>st</sub>, and the OLED may vary widely. In addition, additional thin-film transistors may be included in one pixel PX.

[0056] Hereinafter, the OLED display according to an exemplary embodiment of the present invention will be described in detail.

[0057] FIG. 2 is a cross-sectional view of the OLED display according to an exemplary embodiment of the present invention.

[0058] Referring to FIG. 2, the OLED display according to the present exemplary embodiment includes a substrate 110, a driving thin-film transistor Q<sub>d</sub> formed on the substrate 110,

a pixel electrode **191** connected to the driving thin-film transistor Qd, an organic emission layer **370** formed on the pixel electrode **191**, and a common electrode **270** formed on the organic emission layer **370**.

**[0059]** The substrate **110** is an insulating substrate that is formed of glass, quartz, ceramic, plastic, etc. The substrate **110** may alternatively be a metallic substrate that is formed of stainless steel or the like.

**[0060]** A buffer layer **120** may be disposed on the substrate **110**. The buffer layer **120** may be formed as a single layer of silicon nitride ( $\text{SiN}_x$ ), or a dual layer of silicon nitride ( $\text{SiN}_x$ ) and silicon oxide ( $\text{SiO}_x$ ). The buffer layer **120** may planarize a surface and prevent permeation of unnecessary materials, such as impurities or moisture. The buffer layer **120** may be omitted.

**[0061]** A driving semiconductor **130** is disposed on the buffer layer **120**. The driving semiconductor **130** may include a polysilicon semiconductor material or an oxide semiconductor material. The driving semiconductor **130** includes a channel region **131** not doped with impurities, and contact doping regions **132** and **133** doped with impurities, which are disposed at opposite sides of the channel region **131**. The contact doping regions **132** and **133** include a source region **132** and a drain region **133**. In this case, the doped impurities may vary depending on a type of the thin-film transistor.

**[0062]** A gate insulating layer **140** is disposed on the driving semiconductor **130**. The gate insulating layer **140** may include an inorganic insulating material, such as silicon nitride ( $\text{SiN}_x$ ) or silicon oxide ( $\text{SiO}_x$ ).

**[0063]** A gate wire including the driving gate electrode **125** is disposed on the gate insulating layer **140**. In this case, the driving gate electrode **125** overlaps at least a portion of the driving semiconductor **130**, in particular, the channel region **131** thereof.

**[0064]** An interlayer insulating layer **160** is disposed on the driving gate electrode **125** and the gate insulating layer **140**. The interlayer insulating layer **160** may include an inorganic insulating material or an organic insulating material.

**[0065]** Contact holes **162** and **164** at least partially exposing the driving semiconductor **130** are formed in the gate insulating layer **140** and in the interlayer insulating layer **160**. Particularly, the contact holes **162** and **164** expose the contact doping regions **132** and **133** of the driving semiconductor **130**.

**[0066]** A data wire including a driving source electrode **173** and a driving drain electrode **175** are formed on the interlayer insulating layer **160**. The driving source electrode **173** and the driving drain electrode **175** are respectively connected to the source and drain regions **132** and **133** of the driving semiconductor **130** via the contact holes **162** and **164**.

**[0067]** In this manner, the driving semiconductor **130**, the driving gate electrode **125**, the driving source electrode **173**, and the driving drain electrode **175** form the driving thin-film transistor Qd. It is noted that, however, the structure of the driving thin-film transistor Qd may be varied. The switching thin-film transistor Qs may include a switching semiconductor, a switching gate electrode, a switching source electrode, and a switching drain electrode.

**[0068]** A passivation layer **180** covering the data wire is disposed on the interlayer insulating layer **160**. The passivation layer **180** may remove a step and planarize a surface

thereof, thereby increasing luminous efficiency of the OLED to be formed thereon. A contact hole **182** at least partially exposing the driving drain electrode **175** is formed in the passivation layer **180**.

**[0069]** The passivation layer **180** may include a polyacrylate resin, an epoxy resin, a phenolic resin, a polyamide resin, a polyimide resin, an unsaturated polyester resin, a polyphenylene resin, a polyphenylene sulfide resin, and benzocyclobutene (BCB). It is noted that, however, either one of the passivation layer **180** and the interlayer insulating layer **160** may be omitted.

**[0070]** The pixel electrode **191** is formed on the passivation layer **180**. The pixel electrode **191** may include a transparent conductive material, such as indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide ( $\text{In}_2\text{O}_3$ ), etc., or a reflective metal such as lithium (Li), calcium (Ca), lithium fluoride/calcium (LiF/Al), lithium fluoride/aluminum (LiF/Al), aluminum (Ag), silver (Ag), magnesium (Mg), gold (Au), etc.

**[0071]** The pixel electrode **191** is electrically connected to the driving drain electrode **175** of the driving thin-film transistor Qd via the contact hole **182** formed in the passivation layer **180**, and may be the anode of the OLED. Although not illustrated, the pixel electrode **191** may include first and second transparent electrodes including a transparent conductive material, and a transfective layer disposed between the first and second transparent electrodes, to form a microcavity along with the common electrode **270**.

**[0072]** A pixel defining layer **350** is formed on the passivation layer **180** and an edge portion of the pixel electrode **191**. The pixel defining layer **350** includes a pixel opening **351** that exposes the pixel electrode **191**. The pixel defining layer **350** may include a polyacrylate resin, a polyimide resin, a silica-based inorganic material, etc.

**[0073]** An organic emission layer **370** is formed in the pixel opening **351** of the pixel defining layer **350**. The organic emission layer **370** may include at least one of an emission layer, a hole injection layer (HIL), a hole transporting layer (HTL), an electron transporting layer (ETL), and an electron injection layer (EIL). A structure of the organic emission layer **370** will be described in detail with reference to FIG. 3.

**[0074]** The organic emission layer **370** may include a red organic emission layer for emitting red light, a green organic emission layer for emitting green light, and a blue organic emission layer for emitting blue light. The red organic emission layer, the green organic emission layer, and the blue organic emission layer are respectively formed on red, green, and blue pixels, to implement a color image.

**[0075]** Alternatively, in the organic emission layer **370**, a color image may be implemented by disposing all of the red, green, and blue organic light emission layers on the red pixel, the green pixel, and the blue pixel, and then forming red, green, and blue color filters for each pixel. As another example, a color image may be implemented by forming a white organic emission layer emitting white light on all of the red, green, and blue pixels, and respectively forming red, green, and blue color filters for each pixel. When the color image is implemented by using the white organic emission layer and the color filter, a deposition mask for depositing the red, green, and blue organic emission layers on each pixel, that is, the red, green, and blue pixels, may not be utilized.

**[0076]** The white organic emission layer described above may be formed as a single organic emission layer, and may further include a structure for emitting white light by disposing multiple organic emission layers. For example, a structure for emitting white light by combining at least one yellow organic emission layer with at least one blue organic emission layer, at least one cyan organic emission layer with at least one red organic emission layer, and at least one magenta organic emission layer with at least one green organic emission layer may be included.

**[0077]** The common electrode **270** is formed on the pixel defining layer **350** and on the organic emission layer **370**. The common electrode **270** may include a transparent conductive material, such as indium tin oxide (ITO), indium zinc oxide (IZO), zinc oxide (ZnO), indium oxide (In<sub>2</sub>O<sub>3</sub>), etc., or a reflective metal such as lithium (Li), calcium (Ca), lithium fluoride/calcium (LiF/Al), lithium fluoride/aluminum (LiF/Al), aluminum (Ag), silver (Ag), magnesium (Mg), gold (Au), etc. The common electrode **270** may be the cathode of the OLED. The pixel electrode **191**, the organic emission layer **370**, and the common electrode **270** form the OLED.

**[0078]** A capping layer **500** is disposed on the common electrode **270**. The capping layer **500** is formed in a top emission type of OLED, to prevent incident light on the common electrode **270** from being totally reflected, and, thus, experiencing light loss. The capping layer **500** may include an organic material or an inorganic material. For example, the capping layer **500** may include a triamine derivative, an arylenediamine derivative, CBP, tris(8-hydroxyquinoline) aluminum (Alq<sub>3</sub>), etc.

**[0079]** An optical thickness of the capping layer **500** may be about 1100 Å or more and about 1400 Å or less. As used herein, an optical thickness may be defined by the physical thickness multiplied by the refractive index thereof. A refractive index of the capping layer **500** may be about 1.6 or more and about 2.6 or less. For example, when the refractive index of the capping layer **500** is about 1.87, a physical thickness of the capping layer **500** may be about 600 Å or more and about 730 Å or less. That is, the optical thickness of the capping layer **500** is about 600 Å\*1.87 or more and about 730 Å\*1.87 or less. When the refractive index of the capping layer **500** is about 1.6, the physical thickness of the capping layer **500** may be about 700 Å or more and about 860 Å or less. When the refractive index of the capping layer **500** is about 2.6, the physical thickness of the capping layer **500** is about 430 Å or more and about 530 Å or less. Accordingly, a value range of the desirable physical thickness of the capping layer **500** may vary, depending on the refractive index of the capping layer **500**.

**[0080]** A value of the optical thickness represents the optical thickness at a wavelength of about 550 nm. That is, the optical thickness is a value obtained by multiplying the physical thickness by the refractive index, when light of about 550 nm passes through the capping layer **500**.

**[0081]** Although not illustrated, a thin film encapsulation layer may be further formed on the capping layer **500**. The thin film encapsulation layer may seal and protect the OLED and the driving circuit unit disposed on the substrate **110** from the outside.

**[0082]** Hereinafter, the OLED according to an exemplary embodiment of the present invention will be described in detail.

**[0083]** FIG. 3 is an enlarged cross-sectional view of the OLED of the OLED display according to an exemplary embodiment of the present invention.

**[0084]** The OLED of the OLED display according to the present exemplary embodiment (indicated as X in FIG. 2) has a structure, in which the pixel electrode **191**, the hole injection layer **371**, the hole transporting layer **372**, the emission layer **373**, the electron transporting layer **374**, the electron injection layer **375**, the common electrode **270**, and the capping layer **500** are sequentially disposed. That is, the organic emission layer **370** of FIG. 2 includes the hole injection layer **371**, the hole transporting layer **372**, the emission layer **373**, the electron transporting layer **374**, and the electron injection layer **375** of FIG. 3.

**[0085]** The hole injection layer **371** may be formed on the pixel electrode **191**. In this case, the hole injection layer **371** may be an arbitrary layer for improving injection of holes from the pixel electrode **191** into the hole transporting layer **372**. The hole injection layer **371** may include copper phthalocyanine (CuPc), poly(3,4-ethylenedioxythiophene) (PEDOT), Polyaniline (PANT), N,N-dinaphthyl-N,N'-diphenyl benzidine (NPD), and the like.

**[0086]** The hole transporting layer **372** may be formed on the hole injection layer **371**. The hole transporting layer **372** may perform a function of smoothly transporting the holes transmitted from the hole injection layer **371**. For example, the hole transporting layer **372** may include N,N-dinaphthyl-N,N'-diphenyl benzidine (NPD), TPD (N,N'-bis-(3-methylphenyl)-N,N'-bis-(phenyl)-benzidine), s-TAD, (4,4',4''-tris (N-3-methylphenyl-N-phenyl-amino)-triphenylamine), and the like.

**[0087]** In the present exemplary embodiment, the hole transporting layer **372** is described as to be disposed on the hole injection layer **371**, thus forming a laminated structure. The hole injection layer **371** and the hole transporting layer **372** may alternatively be formed as a single layer.

**[0088]** Although not illustrated, a buffer layer (not shown) may be further disposed on the hole transporting layer **372**. The buffer layer may adjust an amount of holes transferred to the emission layer **373** from the pixel electrode **191**, in addition to an amount of electrons penetrating the hole transporting layer **372** from the emission layer **373**. That is, the buffer layer may control the amount of holes and block the electrons, help to combine the hole and the electrons in the emission layer **373**, and block the electrons from penetrating the hole transporting layer **372**, thereby preventing the hole transporting layer **372** from being damaged by the electrons.

**[0089]** The emission layer **373** includes a light-emitting material that exhibits a specific color. For example, the emission layer **373** may exhibit primary colors, such as blue, green, and red, or a combination of these colors. According to the present exemplary embodiment, the emission layer **373** may include a blue emission layer, a green emission layer, and a red emission layer.

**[0090]** The emission layer **373** includes a host and a dopant. The emission layer **373** may include a material that emits red, green, blue, and white light, and may be formed using a phosphorescent or fluorescent material.

**[0091]** When emitting red light, the emission layer **373** includes a host material that includes CBP (carbazole biphenyl) or mCP (1,3-bis(carbazol-9-yl)), and may be made of a phosphorescent material including a dopant, which includes at least one of PIQIr(acac) (bis(1-phenylisoquinoline)acety-

lactonate iridium), PQIr(acac) (bis(1-phenylquinoline) acetylacetonate iridium), PQIr (tris(1-phenylquinoline) iridium), and PtOEP (octaethylporphyrin platinum), or alternatively, may be made of a fluorescent material including PBD:Eu(DBM)3(Phen) or perylene.

**[0092]** When emitting green light, the emission layer 373 includes a host material that includes CBP or mCP, and may be made of a phosphorescent material that includes a dopant material, which includes Ir(ppy)<sub>3</sub> (fac-tris(2-phenylpyridine)iridium), or alternatively, may be made of a fluorescent material including Alq<sub>3</sub> (tris(8-hydroxyquinolino)aluminum).

**[0093]** When emitting blue light, the emission layer 373 includes a host material including CBP or mCP, and may be made of a phosphorescent material that includes a dopant material, which includes (4,6-F2ppy)<sub>2</sub>Irpic. Alternatively, the emission layer 373 may be made of a fluorescent material including at least one of spiro-DPVBi, spiro-6P, distyryl benzene (DSB), distyrylarylene (DSA), a PFO-based polymer, and a PPV-based polymer.

**[0094]** The electron transporting layer 374 may be disposed on the emission layer 373. In this case, the electron transporting layer 374 may transfer electrons from the common electrode 270 to the emission layer 373. The electron transporting layer 374 may prevent holes injected from the pixel electrode 191 from passing through the emission layer 373 and then moving to the common electrode 270. That is, the electron transporting layer 374 serves as a hole blocking layer, and helps to combine the holes and electrons in the emission layer 373. In this case, the electron transporting layer 374 may include at least one of tris(8-hydroxyquinolino)aluminum (Alq<sub>3</sub>), PBD, TAZ, Spiro-PBD, BAQ, and SAQ.

**[0095]** The electron injection layer 375 is disposed on the electron transporting layer 374. The electron injection layer 375 is an arbitrary layer that may improve the injection of the electrons from the common electrode 270 into the electron transporting layer 374. The electron injection layer 375 may include Alq<sub>3</sub>, LiF, a gallium (Ga) complex, PBD, etc.

**[0096]** Hereinafter, luminous efficiency associated with a thickness of the capping layer of the OLED display according to an exemplary embodiment of the present invention will be described with reference to FIGS. 4 to 7.

**[0097]** FIG. 4 is a graph showing luminous efficiency of white light associated with a thickness of a capping layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 5 is a graph showing luminous efficiency of red light associated with the thickness of the capping layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 6 is a graph showing luminous efficiency of green light associated with the thickness of the capping layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 7 is a graph showing luminous efficiency of blue light associated with the thickness of the capping layer of the OLED display according to an exemplary embodiment of the present invention.

**[0098]** As shown in FIG. 4, as the thickness of the capping layer increases from about 350 Å to about 830 Å, luminous efficiency of white light gradually increases. When the thickness of the capping layer increases at a point where the thickness of the capping layer is about 830 Å, the luminous efficiency of white light gradually decreases.

**[0099]** As shown in FIG. 5, as the thickness of the capping layer increases from about 350 Å to about 900 Å, luminous efficiency of red light gradually increases.

**[0100]** As shown in FIG. 6, as the thickness of the capping layer increases from about 350 Å to about 850 Å, luminous efficiency of green light gradually increases.

**[0101]** As shown in FIG. 7, as the thickness of the capping layer increases from about 350 Å to about 700 Å, luminous efficiency of blue light gradually increases. When the thickness of the capping layer increases from a point where the thickness of the capping layer is about 700 Å, the luminous efficiency of blue light gradually decreases.

**[0102]** Referring to the graphs illustrated in FIGS. 4 to 7, it can be seen that the luminous efficiency is affected as the thickness of the capping layer of the OLED display varies. In particular, as the thickness of the capping layer 500 increases, the luminous efficiency gradually increases, and if it exceeds a predetermined thickness, the luminous efficiency gradually decreases. Luminous efficiency of red or green light continues to gradually increase as the thickness of the capping layer increases. As the thickness of the capping layer increases, the luminous efficiency of blue light increases, but then gradually decreases once it exceeds a predetermined thickness. Characteristics of luminous efficiency of white light substantially reflect those of blue light. This is because the blue pixel represents about half the total power consumption of the red, green, and blue pixels. Accordingly, when power consumption of the blue pixel is reduced, the entire power consumption of the pixels may be reduced, thereby increasing luminous efficiency of white light.

**[0103]** When luminous efficiency of white light is the highest, the thickness of the capping layer is about 830 Å. When the thickness of the capping layer is about 830 Å, luminous efficiency of blue light is about 100%. Luminous efficiency of blue light is about the similar level, i.e., 100%, when the thickness of the capping layer is about 600 Å. Since the luminous efficiency of the blue pixel is closely associated with the entire luminous efficiency of the pixels, the thickness of the capping layer may be about 600 Å or more. When the thickness of the capping layer is about 600 Å or less, the luminous efficiency significantly decreases and thus the power consumption increases.

**[0104]** In FIGS. 4 to 7, the thickness of the capping layer represents a physical thickness, and the graphs of FIGS. 4 to 7 show experimental results when a refractive index thereof is about 1.87. Accordingly, the optical thickness of the capping layer may be about 600 Å\*1.87 or more, i.e., about 1100 Å or more.

**[0105]** Hereinafter, an amount of color shift associated with the thickness of the capping layer of the OLED display according to an exemplary embodiment of the present invention will be described with reference to FIGS. 8 to 11. As used herein, the amount of color shift may be a variation in color that is associated with a viewing angle, which represents a difference in color between a front view and a side view. As the amount of color shift increases, the difference in color associated with the viewing angle increases. When the amount of color shift is reduced, the viewing angle may be improved. The amount of color shift is digitized as u'v'.

**[0106]** FIG. 8 is a graph showing an amount of color shift for white associated with the thickness of the capping layer of the OLED display according to an exemplary embodi-

ment of the present invention. FIG. 9 is a graph showing an amount of color shift for red associated with the thickness of the capping layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 10 is a graph showing an amount of color shift for green associated with the thickness of the capping layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 11 is a graph showing an amount of color shift for blue associated with the thickness of the capping layer of the OLED display according to an exemplary embodiment of the present invention.

[0107] As shown in FIG. 8, when the thickness of the capping layer is about 830 Å to about 730 Å, an amount of color shift for white shows similar levels, and when the thickness of the capping layer is about 730 Å or less, the amount of color shift for white gradually decreases as the thickness of the capping layer decreases. When the thickness of the capping layer decreases from about 830 Å to about 780 Å, the amount of color shift for white slightly increases, and when the thickness of the capping layer is about 730 Å, the amount of color shift for white shows a level similar to that of about 830 Å.

[0108] As shown in FIG. 9, when the thickness of the capping layer decreases from about 830 Å to about 680 Å, an amount of color shift for red decreases from about 0.058 to about 0.039.

[0109] As shown in FIG. 10, when the thickness of the capping layer decreases from about 830 Å to about 680 Å, an amount of color shift for green decreases from about 0.023 to about 0.011.

[0110] As shown in FIG. 11, when the thickness of the capping layer decreases from about 830 Å to about 680 Å, an amount of color shift for blue decreases from about 0.037 to about 0.027.

[0111] Referring to the graphs illustrated in FIGS. 8 to 11, it can be seen that the amount of color shift is affected as the thickness of the capping layer of the OLED display varies. Generally, as the thickness of the capping layer decreases, the amount of color shift gradually decreases. The amount of color shift for white represents the amount of color shift associated with the various thicknesses of the capping layer, and when the thickness of the capping layer decreases for the red, green, and blue colors, the amounts of color shift are respectively reduced.

[0112] When the thickness of the capping layer is about 830 Å, the amount of color shift for white is about 0.022. The amount of color shift for white is about similar level, i.e., 0.022, when the thickness of the capping layer is about 730 Å. In order to have a lower amount of color shift than when the thickness of the capping layer is about 830 Å, the thickness of the capping layer may be about 730 Å or less.

[0113] In FIGS. 8 to 11, the thickness of the capping layer represents a physical thickness and the graphs of FIGS. 8 to 11 show experimental results when a refractive index thereof is about 1.87. Accordingly, an optical thickness of the capping layer may be about 730 Å\*1.87 or less, i.e., about 1400 Å or less.

[0114] Through the analysis of the graphs of FIGS. 4 to 11, the optical thickness of the capping layer may be about 600 Å\*1.87 or more and about 730 Å\*1.87 or less. That is, the optical thickness of the capping layer may be about 1100 Å or more and about 1400 Å or less, to improve luminous efficiency and minimize the amount of color shift associated with the viewing angle.

[0115] Hereinafter, the OLED display according to an exemplary embodiment of the present invention will be described with reference to FIG. 12.

[0116] The OLED display according to the present exemplary embodiment illustrated in FIG. 12 has substantially the same configuration as the OLED display illustrated with reference to FIGS. 1 to 3, and, thus, repeated description thereof will be omitted. The present exemplary embodiment differs from the aforementioned OLED display in that an emission layer includes a layer including only a host, which will be described below in detail.

[0117] FIG. 12 is a cross-sectional view of an organic emission layer of the OLED display according to an exemplary embodiment of the present invention.

[0118] As shown in FIG. 12, the organic emission layer 370 of the OLED display according to the present exemplary embodiment includes the hole injection layer 371, the hole transporting layer 372, the emission layer 373, the electron transporting layer 374, and the electron injection layer 375. The hole injection layer 371, the hole transporting layer 372, the emission layer 373, the electron transporting layer 374, and the electron injection layer 375 may be sequentially disposed. It is noted that, however, a portion the layers above constituting the organic emission layer 370 may be omitted.

[0119] The emission layer 373 includes a first emission layer 373a and a second emission layer 373b. The first emission layer 373a includes a host, but not a dopant, and, thus, may be referred to as a non-doping layer. The second emission layer 373b includes a host and a dopant, and, thus, may be referred to as a doping layer. The first emission layer 373a contacts the hole transporting layer 372, and the second emission layer 373b contacts the electron transporting layer 374. It is noted that, however, the first emission layer 373a may alternatively contact the hole injection layer 371 and the second emission layer 373b may contact the electron injection layer 375. In this case, the hole transporting layer 372 or the electron transporting layer 374 may be omitted.

[0120] The second emission layer 373b is disposed in center and upper regions of the emission layer 373. A thickness ratio of the first emission layer 373a to the second emission layer 373b may be about 1:3. It is noted that, however, the thickness ratio of the first emission layer 373a to the second emission layer 373b may widely vary.

[0121] In the present exemplary embodiment, a driving voltage may be reduced, since the emission layer 373 includes the non-doping layer that includes the host but not the dopant, thereby increasing the luminous efficiency and reducing the color shift associated with the viewing angle.

[0122] The OLED display according to the present exemplary embodiment may include pixels. In this case, the pixels may include first color pixels PX1, second color pixels PX2, and third color pixels PX3, as shown in FIG. 24. For example, the first color pixel PX1 may be a green pixel, the second color pixel PX2 may be a red pixel, and the third color pixel PX3 may be a blue pixel. In this case, only an emission layer 373 of the first color pixel PX1 may include the first emission layer 373a and the second emission layer 373b, and emission layers 373 of the second PX2 and third color pixels PX3 may include only the second emission layer 373b.

[0123] That is, only the green pixel may have a structure in which the emission layer 373 is partially doped. This is because the red and blue pixels may not be affected by the

reduced driving voltage as the green pixel. It is noted that, however, all of the first color pixel, second color pixel, and third color pixel may alternatively have a structure in which the emission layer 373 is partially doped. In the above description, the first emission layer 373a and the second emission layer 373b are interchangeable with each other.

[0124] Hereinafter, an organic emission layer of an OLED display according to an exemplary embodiment of the present invention will be described with reference to FIG. 13.

[0125] FIG. 13 is a cross-sectional view of an organic emission layer of the OLED display according to an exemplary embodiment of the present invention.

[0126] Referring to FIG. 13, the organic emission layer 370 of the OLED display according to the present exemplary embodiment includes a hole injection layer 371, a hole transporting layer 372, an emission layer 373, an electron transporting layer 374, and an electron injection layer 375.

[0127] The emission layer 373 includes a first emission layer 373a including a host, but not a dopant, and a second emission layer 373b including a host and a dopant. The first emission layer 373a contacts the hole transporting layer 372, and also contacts the electron transporting layer 374. It is noted that, however, the first emission layer 373a may alternatively contact the hole injection layer 371, and may also contact the electron injection layer 375. In this case, the hole transporting layer 372 or the electron transporting layer 374 may be omitted.

[0128] The second emission layer 373b is disposed in a center region of the emission layer 373. The first emission layers 373a are disposed above and below the second emission layer 373b. A thickness ratio of the first emission layer 373a to the second emission layer 373b may be about 1:1. It is noted that, however, the thickness ratio of the first emission layer 373a to the second emission layer 373b may widely vary.

[0129] For comparison with the present exemplary embodiment, a structure of an OLED display according to a comparative embodiments will be described with reference to FIGS. 14 and 15.

[0130] FIGS. 14 and 15 are cross-sectional views of an organic emission layer of an OLED display according to comparative embodiments.

[0131] Referring to FIG. 14, the emission layer 373 includes a first emission layer 373a including a host, but not a dopant, and a second emission layer 373b including a host and a dopant. The first emission layer 373a contacts an electron transporting layer 374, and the second emission layer 373b contacts a hole transporting layer 372. The second emission layer 373b is disposed in center and lower regions of the emission layer 373. A thickness ratio of the first emission layer 373a to the second emission layer 373b may be about 1:3.

[0132] Referring to FIG. 15, the emission layer 373 includes the first emission layer 373a that includes a host, but not a dopant, and the second emission layer 373b that includes a host and a dopant. The second emission layer 373b contacts the hole transporting layer 372, and also contacts the electron transporting layer 374. The first emission layer 373a is disposed in a center region of the emission layer 373. The second emission layers 373b are disposed above and below the first emission layer 373a. A thickness ratio of the first emission layer 373a to the second emission layer 373b may be about 1:1.

[0133] Referring to FIG. 16, an amount of color shift of the OLED display according to an exemplary embodiment of the present invention will be described.

[0134] FIG. 16 is a graph showing an amount of color shift associated with a structure of the emission layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 16 shows an amount of color shift for green associated with a structure of an emission layer of a green pixel.

[0135] In a structure where the emission layer is entirely doped by a doping layer that includes a host and a dopant, the amount of color shift according to the viewing angle is about 0.013. In a structure where the emission layer is partially doped by a non-doping layer that includes a host, but not a dopant, the amount of color shift associated with the viewing angle is about 0.011. It can be seen that in the structure where the emission layer is partially doped, the amount of color shift associated with the viewing angle improves by about 16%.

[0136] In addition, when the optical thickness of the capping layer is about 1122 Å (600 Å\*1.87) or more and about 1365.1 Å (730 Å\*1.87) or less, and the emission layer has the structure where the emission layer is partially doped, the amount of color shift associated with the viewing angle may be further improved.

[0137] Hereinafter, a driving voltage and luminous efficiency of the OLED display according to an exemplary embodiment of the present invention will be described with reference to FIGS. 17 and 18.

[0138] FIG. 17 is a graph showing a driving voltage associated with the structure of the emission layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 18 is a graph showing luminous efficiency associated with the structure of the emission layer of the OLED display according to an exemplary embodiment of the present invention.

[0139] Referring to FIG. 17, in the structure where the emission layer is entirely doped by a doping layer including a host and a dopant (Ref.), a driving voltage thereof is about 6.5V. A driving voltage of the OLED display according to the exemplary embodiment of present invention illustrated with reference to FIG. 12 is about 5.7V (case 1). That is, in the structure where the first emission layer contacts the hole transporting layer, the second emission layer contacts the electron transporting layer, and the thickness ratio of the first emission layer to the second emission layer is about 1:3, a driving voltage is decreased further than in the structure where the emission layer is entirely doped (Ref.). A driving voltage of the OLED display according to the exemplary embodiment of the present invention illustrated with reference to FIG. 13 is about 5.5V (case 2). That is, in the structure where the first emission layer contacts the hole transporting layer and the electron transporting layer, the second emission layer is disposed between the first emission layers, and the thickness ratio of the first emission layer to the second emission layer is about 1:1, a driving voltage is decreased further than in the structure where the emission layer is entirely doped (Ref.).

[0140] A driving voltage of the OLED display according to the comparative embodiment illustrated with reference to FIG. 13 is about 6.2V (case 3). That is, in the structure where the first emission layer contacts the electron transporting layer, the second emission layer contacts the hole transporting layer, and a thickness ratio of the first emission layer to

the second emission layer is about 1:3, a driving voltage is decreased further than in the structure where the emission layer is entirely doped (Ref.), but the difference between the driving voltages is not significant. A driving voltage of the OLED display according to the comparative embodiment illustrated with reference to FIG. 15 is about 6.9V (case 4). That is, in the structure where the second emission layer contacts the hole transporting layer and the electron transporting layer, the first emission layer is disposed between the second emission layers, and the thickness ratio of the first emission layer to the second emission layer is about 1:1, a driving voltage is increased further than in the structure where the emission layer is entirely doped (Ref.).

[0141] Referring to FIG. 18, in the structure where the emission layer is entirely doped (Ref.) by including a doping layer that includes a host and a dopant, luminous efficiency is about 113%. In case 1 illustrated with reference to FIG. 12, luminous efficiency thereof is about 122%. That is, in the structure where the first emission layer contacts the hole transporting layer, the second emission layer contacts the electron transporting layer, and the thickness ratio of the first emission layer and the second emission layer is about 1:3, the luminous efficiency increases further than in the structure where the emission layer is entirely doped (Ref.). In case 2 illustrated with reference to FIG. 13, luminous efficiency thereof is about 125%. That is, in the structure where the first emission layer contacts the hole transporting layer and the electron transporting layer, the second emission layer is disposed between the first emission layers, and the thickness ratio of the first emission layer and the second emission layer is about 1:1, luminous efficiency increases further than in the structure where the emission layer is entirely doped (Ref.).

[0142] In case 3, illustrated with reference to FIG. 14, luminous efficiency thereof is about 115%. That is, in the structure where the first emission layer contacts the electron transporting layer, the second emission layer contacts the hole transporting layer, and the thickness ratio of the first emission layer to the second emission layer about 1:3, the luminous efficiency increases further than in the structure where the emission layer is entirely doped (Ref.), but the luminous efficiency is slightly different therefrom. In case 4, illustrated with reference to FIG. 15, luminous efficiency thereof is about 96%. That is, in the structure where the second emission layer contacts the hole transporting layer and the electron transporting layer, the first emission layer is disposed between the second emission layers, and the thickness ratio of the first emission layer to the second emission layer is about 1:1, the luminous efficiency decreases further than in the structure where the emission layer is entirely doped (Ref.).

[0143] Referring to FIGS. 17 and 18, where the emission layer is partially doped, the second emission layer is disposed in the center region of the emission layer, and the first emission layer contacts the hole injection layer or the hole transporting layer, according to the present exemplary embodiment, the driving voltage thereof may be decreased and luminous efficiency thereof may be increased further than in the structure where the emission layer is entirely doped. Even if the emission layer has the structure where the emission layer is partially doped (as in the Ref.), an effect of the reduced driving voltage may not be significant, or the

driving voltage may be increased in the structure where the second emission layer contacts the hole injection layer or the hole transporting layer.

[0144] As described above, when the optical thickness of the capping layer is about 1122 Å ( $600 \text{ Å} \times 1.87$ ) or more and about 1365.1 Å ( $730 \text{ Å} \times 1.87$ ) or less, the emission layer is partially doped, the second emission layer is disposed in the center region of the emission layer, and the first emission layer contacts the hole injection layer or the hole transporting layer, the driving voltage thereof may be further decreased and the luminous efficiency can be further increased.

[0145] Hereinafter, an OLED display according to an exemplary embodiment of the present invention will be described with reference to FIG. 19.

[0146] Referring to FIG. 19, the OLED display according to the present exemplary embodiment has substantially the same configuration as the OLED display illustrated with reference to FIGS. 1 to 3, and, thus, detailed description thereof will be omitted. The present exemplary embodiment differs from the aforementioned OLED display in that a capping layer includes multiple layers, which will be described below in detail.

[0147] FIG. 19 is a cross-sectional view of an OLED of the OLED display according to an exemplary embodiment of the present invention.

[0148] The OLED according to the present exemplary embodiment has a structure, in which a pixel electrode 191, a hole injection layer 371, a hole transporting layer 372, an emission layer 373, an electron transporting layer 374, an electron injection layer 375, a common electrode 270, and a capping layer 500 are sequentially disposed.

[0149] The capping layer 500 includes first and second capping layers 510 and 520 that have different refractive indices from each other. The second capping layer 520 is disposed on the first capping layer 510. A refractive index of the second capping layer 520 is higher than a refractive index of the first capping layer 510. Accordingly, the first and second capping layers 510 and 520 include different materials. A difference between the refractive index of the first capping layer 510 and the refractive index of the second capping layer 520 is about 0.1 or more and about 1.4 or less. The refractive index of the first capping layer 510 may be about 1.2 or more and about 1.5 or less. The refractive index of the second capping layer 520 may be about 1.6 or more and about 2.6 or less. For example, the refractive index of the first capping layer 510 may be about 1.38, and the refractive index of the second capping layer 520 may be about 2.05.

[0150] An optical thickness of the first capping layer 510 may be about 60 Å or more and about 210 Å or less. When the refractive index of the first capping layer 510 is about 1.38, a physical thickness of the first capping layer 510 may be about 50 Å or more and about 150 Å or less. That is, the optical thickness of the first capping layer 510 may be about  $50 \text{ Å} \times 1.38$  or more and about  $150 \text{ Å} \times 1.38$  or less. A value range of the desirable physical thickness of the first capping layer 510 may vary depending on the refractive index of the first capping layer 510.

[0151] An optical thickness of the second capping layer 520 may be about 820 Å or more and about 1030 Å or less. When the refractive index of the second capping layer 520 is 2.05 or more, a physical thickness of the second capping layer 520 may be about 400 Å or more and about 500 Å or

less. That is, the optical thickness of the second capping layer 520 may be about  $400 \text{ \AA} \times 2.05$  or more and about  $500 \text{ \AA} \times 2.05$  or less. A value range of the desirable physical thickness of the second capping layer 520 may vary depending on the refractive index of the second capping layer 520. As used herein, a value of the optical thickness may represent the optical thickness at a wavelength of about 550 nm. That is, the optical thickness is a value that is obtained by multiplying the physical thickness with the refractive index when light of about 550 nm passes through the capping layer 500.

[0152] Hereinafter, luminous efficiency associated with thicknesses of first and second capping layers of the OLED according to an exemplary embodiment of the present invention will be described with reference to FIGS. 20 and 21.

[0153] FIG. 20 is a graph showing luminous efficiency associated with a thickness of a first capping layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 21 is a graph showing luminous efficiency associated with a thickness of a second capping layer of the OLED display according to an exemplary embodiment of the present invention.

[0154] Referring to FIG. 20, as a first thickness of the first capping layer increases from about 50 Å to about 150 Å, luminous efficiency gradually increases. When the first thickness of the first capping layer increases from a point where the first thickness of the first capping layer is about 150 Å, the luminous efficiency gradually decreases.

[0155] Referring to FIG. 21, as a second thickness of the second capping layer increases from about 400 Å to about 500 Å, the luminous efficiency gradually increases. When the second thickness of the second capping layer increases from a point where the second thickness of the second capping layer is about 500 Å, the luminous efficiency gradually decreases.

[0156] Hereinafter, an amount of color shift associated with the thicknesses of the first and second capping layers of the OLED display according to an exemplary embodiment of the present invention will be described with reference to FIGS. 22 and 23.

[0157] FIG. 22 is a graph showing an amount of color shift associated with the thickness of the first capping layer of the OLED display according to an exemplary embodiment of the present invention. FIG. 23 is a graph showing amount of color shift associated with the thickness of the second capping layer of the OLED display according to an exemplary embodiment of the present invention.

[0158] Referring to FIG. 22, as the first thickness of the first capping layer increases from about 50 Å to about 150 Å, an amount of color shift gradually increases. When the first thickness of the first capping layer is between about 150 Å and about 200 Å, the amount of color shift does not vary, but the amount of color shift gradually decreases when the first thickness of the first capping layer increases from a point where the first thickness is about 200 Å.

[0159] Referring to FIG. 23, as the second thickness of the second capping layer increases from about 400 Å to about 550 Å, the amount of color shift gradually increases. When the second thickness of the second capping layer increases from a point where the second thickness of the second capping layer is about 550 Å, the amount of color shift gradually decreases.

[0160] In consideration of data from FIGS. 20 to 23, a desirable thickness range of the first and second capping layers may be obtained. When considering only the luminous efficiency, the first thickness of the capping layer may be about 50 Å to about 200 Å, and the second thickness of the capping layer may be about 400 Å to about 600 Å. In the present exemplary embodiment, both the amount of color shift associated with the viewing angle and the luminous efficiency are taken into account. Accordingly, the first thickness of the capping layer may be about 50 Å and about 150 Å, and the second thickness of the capping layer may be about 400 Å and about 500 Å.

[0161] The thicknesses of the first and second capping layers in FIGS. 20 to 23 represent the physical thicknesses, and the graphs of FIGS. 20 to 23 show experimental results when a refractive index of the first capping layer is 1.38 and a refractive index of the second capping layer is 2.05. Accordingly, an optical thickness of the first capping layer may be about  $50 \text{ \AA} \times 1.38$  or more and about  $150 \text{ \AA} \times 1.38$  or less, and an optical thickness of the second capping layer may be about  $400 \text{ \AA} \times 2.05$  or more and about  $500 \text{ \AA} \times 2.05$  or less.

[0162] In addition, the optical thickness of the first capping layer may be about 60 Å or more and about 210 Å or less, and the optical thickness of the second capping layer may be about 820 Å or more and about 1030 Å or less, the emission layer may have the structure where the emission layer is partially doped, the second emission layer may be disposed in the center region of the emission layer, and the first emission layer can contact the hole injection layer or the hole transporting layer, thereby further increasing the luminous efficiency and further improving the amount of color shift associated with the viewing angle.

[0163] Although certain exemplary embodiments and implementations have been described herein, other embodiments and modifications will be apparent from this description. Accordingly, the inventive concept is not limited to such exemplary embodiments, but rather to the broader scope of the presented claims and various obvious modifications and equivalent arrangements.

What is claimed is:

1. An organic light emitting diode (OLED) display, comprising:

a first color pixel, a second color pixel, and a third color pixel on a substrate, each of the first, second, and third color pixels comprising:

a first electrode disposed on the substrate;  
an organic emission layer disposed on the first electrode;  
a second electrode disposed on the organic emission layer; and  
a capping layer disposed on the second electrode,

wherein:

the first color pixel is configured to emit green light, and the second and third color pixels are each configured to emit a color of light other than green;

the organic emission layer of the first color pixel comprises a first emission layer and a second emission layer each being configured to emit light;

the organic emission layer of the second color pixel or the third color pixel comprises a third emission layer configured to emit light;

the second emission layer and the third emission layer comprise both a host and a dopant; and

the first emission layer comprises the host, and does not comprise any dopants therein.

2. The OLED display of claim 1, wherein a refractive index of the capping layer is in a range of about 1.6 to about 2.6.

3. The OLED display of claim 2, wherein a physical thickness of the capping layer is in a range of about 600 Å to about 730 Å when the refractive index of the capping layer is about 1.87.

4. The OLED display of claim 2, wherein the refractive index of the capping layer is measured at a wavelength of about 550 nm.

5. The OLED display of claim 1, wherein the capping layer comprises an organic material or an inorganic material.

6. The OLED display of claim 5, wherein the capping layer comprises at least one of a triamine derivative, an arylendiamine derivative, CBP, and tris(8-hydroxyquinoline) aluminum (Alq3).

7. The OLED display of claim 1, wherein an optical thickness of the capping layer is in a range of about 1100 Å to about 1400 Å.

8. The OLED display of claim 1, wherein the organic emission layer of the first color pixel further comprises:

a hole injection layer and a hole transporting layer disposed on the first electrode; and  
an electron transporting layer and an electron injection layer.

9. The OLED display of claim 8, wherein:

the first emission layer contacts the hole injection layer or the hole transporting layer; and

the second emission layer contacts the electron transporting layer or the electron injection layer.

10. The OLED display of claim 9, wherein:

the organic emission layer of the first color pixel further comprises another first emission layer; and

the second emission layer is disposed between the first emission layer and the another first emission layer.

11. The OLED display of claim 10, wherein the second emission layer is disposed in a center region of the organic emission layer of the first color pixel.

12. The OLED display of claim 10, wherein:

the first emission layer disposed on the second emission layer contacts the hole injection layer or the hole transporting layer; and

the another first emission layer disposed under the second emission layer contacts the electron transporting layer or the electron injection layer.

13. The OLED display of claim 1, wherein:

the organic emission layer of the first color pixel further comprises another first emission layer; and

the second emission layer is disposed between the first emission layer and the another first emission layer.

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